

Speedstack 2017 Q2 Update

Richard Attrill / John Lee – April 2017 (Rev 1)



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Introducing Speedstack 2017

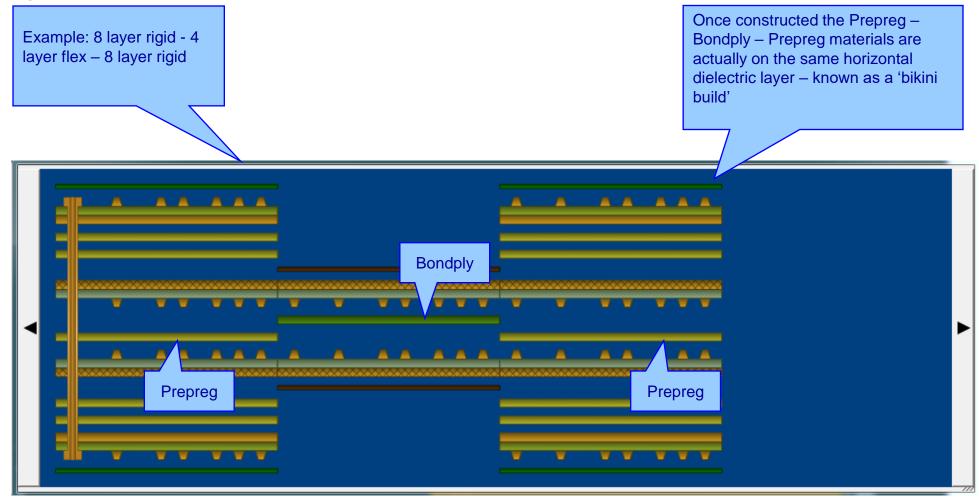
Welcome to a preview of Speedstack 2017.

We have introduced a number of new features that have been requested through our Polarcare software maintenance service.

The following slides provide an overview of these new features. If you would like to have a web-based demonstration please contact your local Polar office, details are shown on the last slide of this presentation.



<u>Speedstack Flex – Improved support for 'bikini builds'</u>





<u>Speedstack Flex – Improved support for 'bikini builds'</u>

Speedstack now provides tools to allow the user to align materials in the same horizontal dielectric layer. The Prepreg and Bondply materials now align as they do in the actual rigid-flex construction

	• •							
				Bondply	 			
	• •		• •		 •	/ v v		
	Prepreg					Prepreg		
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			3					

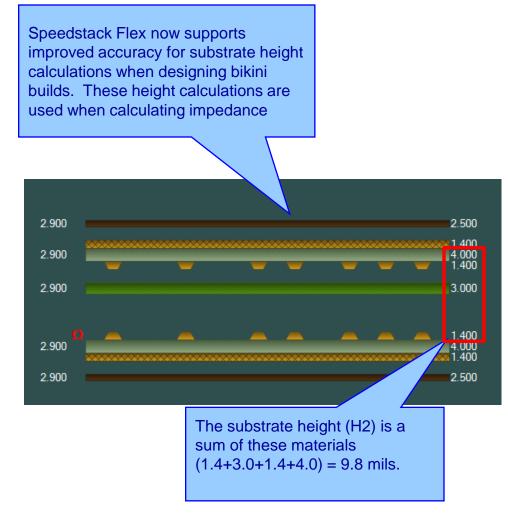


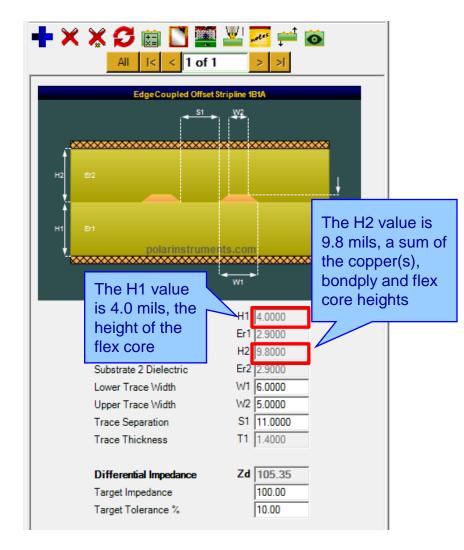
Speedstack Flex – Improved support for 'bikini builds'

C:\Users\Richard Attrill\Desktop\PINC 24082016\3A5487D40_MB	v12.stk Units: Microns					Pola	
		differen dielecti of docu	chnical report a nt materials on ric layer, impro umentation bet igner and fabri	the same ving the ween the	e clarity		
						_	
Rigid 1 Flex 1	Rigid 2						
StackName: Rigid 1	Version:	Revision: Modif	fication: Date of Revision:	Editor			7
Date: Author: Department: Site:	Associated Documents:				Page 1/X		
Crepyright © www.polarinstruments.com							



Speedstack Flex – Improved support for 'bikini builds'



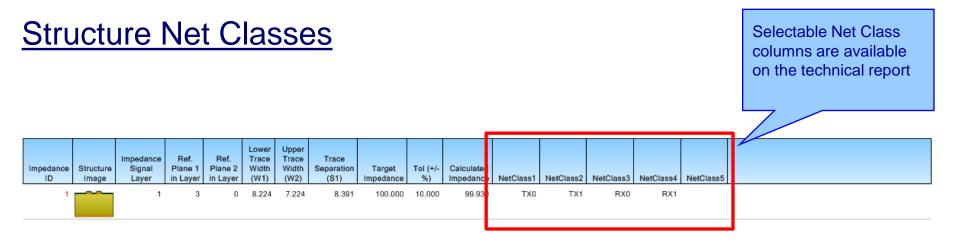




Structure Net Classes	₽ ->-D			Net Class name These net class	e to store up to f s with each struct names provide a impedance nets layout system.	cture. a link
		Structure Net Clas	sses			×
				· · · ·		
H1 81		Net Class 1	TX0			
polarinstrume its.com		Net Class 2	TX1			_
Substrate 1 Height H1 6.3500			,			
Substrate 1 Dielectric Er1 4.2000		Net Class 3	RX0			
Lower Trace Width W1 8.2238						
Upper Trace Width W2 7.2238						
Trace Separation S1 8.3911		Net Class 4	RX1			
Trace Thickness T1 0.7000						
Coating Above Substrate C1 1.1000						
Coating Above Trace C2 1.5000		Net Class 5				
Coating Between Traces C3 1.7500						
Coating Dielectric CEr 4.3000			1			
					Apply Ca	ancel
Differential Impedance Zd 99.93 Target Impedance 100.00						
Target Impedance 100.00 Target Tolerance % 10.00						

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<NetClasses>

<NetClass1>TX0</NetClass1> <NetClass2>TX1</NetClass2> <NetClass3>RX0</NetClass3> <NetClass4>RX1</NetClass4> <NetClass5 /> Speedstack now provides updated import / export XML file formats (STKX and SSX) to support Net Classes.

These file formats are commonly used to pass detailed stack up information to and from CAD / CAM systems

</NetClasses>



Extensive range of XML-based import / export options (STKX / SSX)

File	1	Stack : Eval Imperial Comma Test Attributes Net Classes RCC.stk Project : For the second seco
	Open Stack Ctrl+Shift+O Search Ctrl+F	The 2017 edition will now support STKX v11.00 and v12.00, SSX v1.00
	Save Project Ctrl+S Save Project As Ctrl+Shift+S Save Stack Save Stack As	and v2.00
	Export •	Coupon Generator (CGen)
	Import +	CITS File DXF
	Print +	Gerber
	Properties Ctrl+I	Stackup Image
	Recent Files +	Cadence Allegro
	Exit Ctrl+Q	CSV
- -	Foil CopperFoil PP PrePreg 1080	IPC-2581 Rev B Mentor Graphics
-		XML File (.stlx) V12.00 (Newest)
2	Core FR4 Core	Zuken CR-8000 V11.00 Zuken DFM Centre
	PP PrePreg 3080	Ucamco Integr8tor and Ucam (.ssx)

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Extensive range of XML-based import / export options (STKX / SSX)

- A number of major enhancements have been made introduced to the latest file format:
- Support for the new material attributes and structure net classes
 Support for material colours
- Improved support for structure coating profile (C1, C2, C3, CEr)
 More comprehensive structure parameter information including these extra parameters - <StructureNumber>, <H1>, <Er1>, <H2>,
 <Er2>, <H3>, <Er3>, <H4>, <Er4>, <C1>, <C2>, <C3>, <CEr>,
 <NetClass1>, <NetClass2>, <NetClass3>, <NetClass4>,
 <NetClass5>
- Simplified versioning information



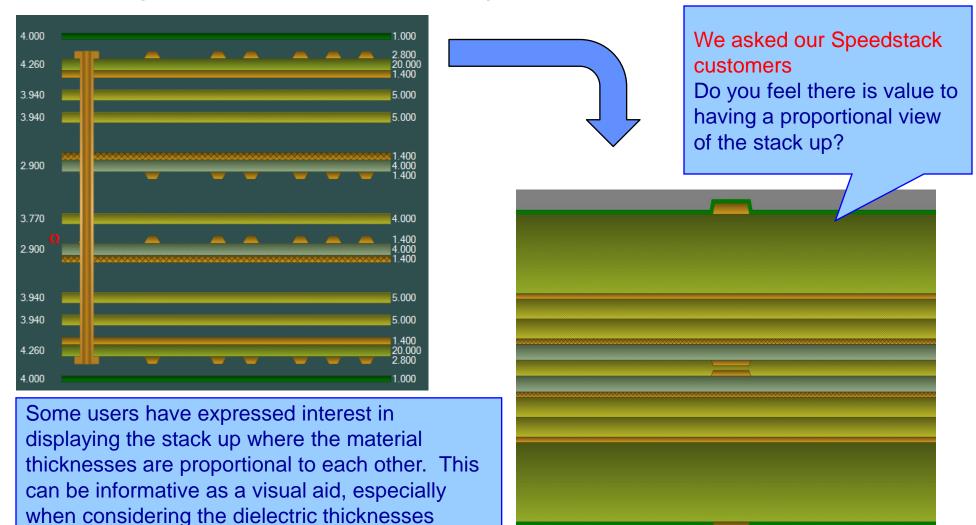


Technical report enhancements

Impedance ID	Structure Image	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Target Impedance	Tol (+/- %)	Calculated Impedance	Trace Pitch (S1+ W1)	Coating Above Substrate (C1)	Coating Above Trace (C2)	Coating Between Traces (C3)	Coating Delectric (CEr)
1		1	3	0	8.224	7.224	8.391	100.000	10.000	99.930	16.615	1.100	1.500	1.750	4.300
2		1	3	0	4.500	3.500	0.000	75.000 umns have	10.000	75.270	0.000	1.100	1.000	0.000	4.300
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Layer		Stack up			Supplier	Sup Descr		Description			Processed Thickness	colu	umn nov	ssed Thio v include k thickn	es the
1		STSL		Pc	olar Sample o <mark>lar Sample</mark> olar Sample	s F(M/001 Liquid D/001 P/001		ole Mask oper Foil reg 1080	SolderMask Copper Dielectric	1.100 0.700 1.950	4			
2 3				Po	olar Sample	s C(D/005	F	R4 Core	FR4	1.400 3.000 1.400				



BETA Experimental Feature : Proportional View



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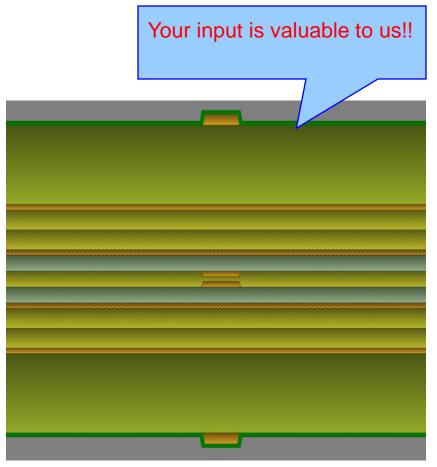
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between electrical layers



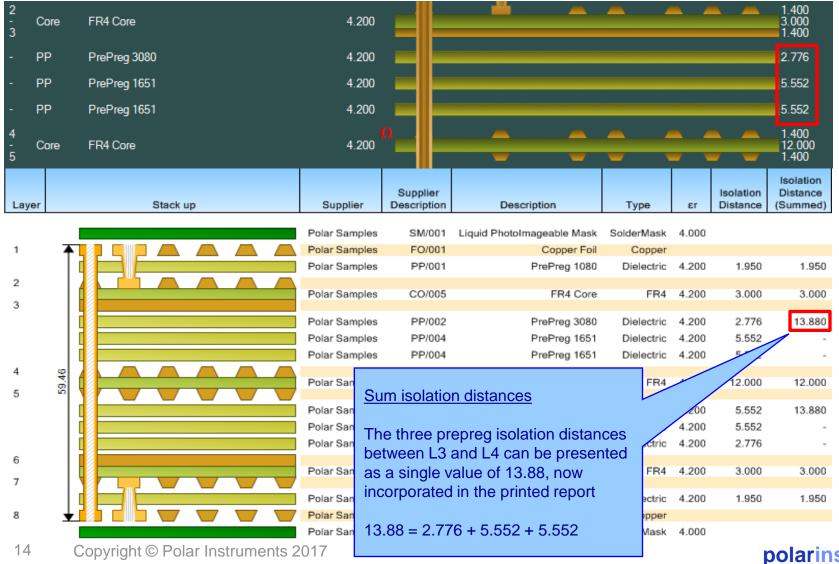
BETA Experimental Feature : Proportional View

Update: We have received positive feedback from existing Speedstack customers in Europe and the US:





Printing : Sum isolation distances





Enhanced library management using Excel™

Rich Rich Rich	Suppler/Decorption F0001 F0002 F0003	Description Copper Fol Copper Fol Copper Fol	Stock Number 100-001 100-002 100-003	Cu Base Thickness 0.7 1.4 2.8	Coper Coper Coper Coper	Cost 1 1 2 3 3 1								cı th	usto le e:	skeo mer xper nain	s ho ienc	w to e of	imp set	orov ting	e up
1						<u>,</u>		Foils_Rich.cs	57		\checkmark	5						7		_	
1						<u> </u>		A	B	c	D	E	F	G	Н		J	К	L	M	1
					_	<u> </u>	1	A *Foil	В	C	D	E	F			I e Notes 1	J Notes 2	K Notes 3	L		
					_	<u> </u>	1 2	A *Foil *Type	B Supplier		D Descriptics Cooper Fc			Cost		I e Notes 1	J Notes 2	K Notes 3	L Notes 4		Size
				_	_		1 2 3	A *Foil *Type Copper	B Supplier Rich	FO/001	D Descriptic Copper Fc 1 Copper Fc 2	100-001	F Cu Thickn 0.7 1.4	Cost	Lead Tim		J Notes 2	K Notes 3	L Notes 4		

These subtle but user friendly changes transform the task of initialising and maintaining Speedstack libraries



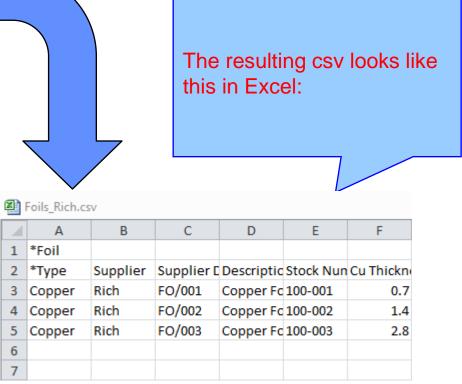
Example takes 3 foils from standard library and rename as "Rich"

		🕎 👎 ¥+ ¥× 🔒			
Foils Prepregs R(Supplier Rich Rich Rich	Cs Cores Solder Masks Ident Supplier Description FO/001 FO/002 FO/003	Inks Peelable Masks Coverlays Bond Ply Description Copper Foil Copper Foil Copper Foil		lets you exp	library function ort to a file, for oils_Rich.csv
			Units for Export Microns Mils/Thous Millimetres	 Comma Tab 	Export Cancel
	is is a library fro anYa or Panaso	om a supplier like Isola nic	C Inches		



Then export to a .csv, for use with Excel

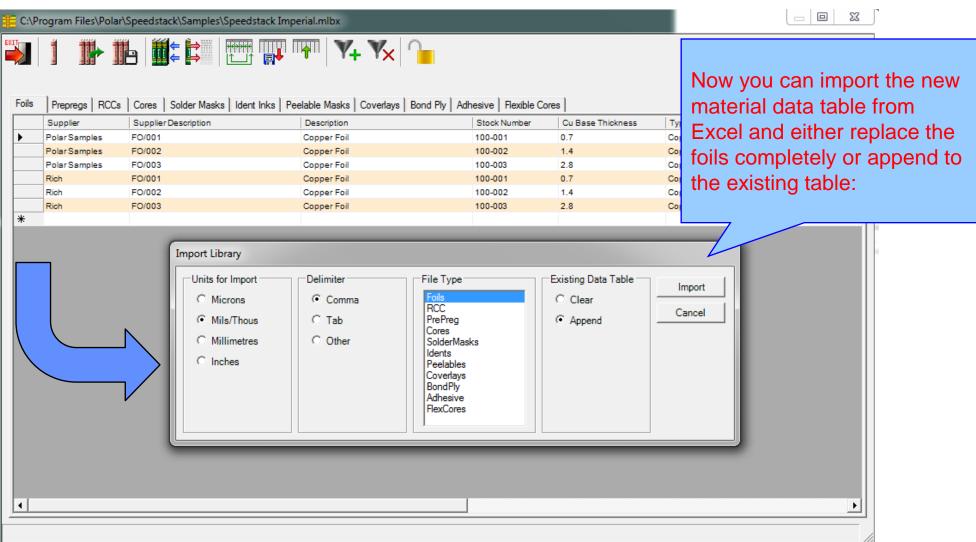
Units for Export O Microns Mils/Thous O Millimetres O Inches	Delimiter © Comma © Tab © Other	Export Cancel		
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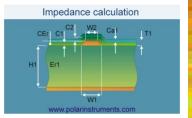
Assume this is a library from a supplier like Isola, Ventec, NanYa or Panasonic

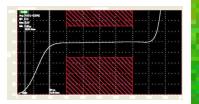


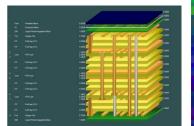
After editing the materials in Excel reload into Speedstack

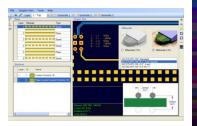


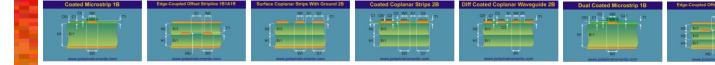










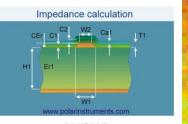


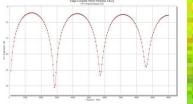
Thank you

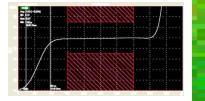


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