

Coated Microstrip 18 Edge-Coupled Offset Striptment 181A1R Surface Coplanar Strips 28 Diff Coated Coplanar Strips 28 Dual Coated Microstrip 18 Edge-Coupled Offset Striptment 281A1R Image: Comparing Stript Stript

Si9000e 2021 - 2023 Updates

Richard Attrill – June 2023 (Rev 4)



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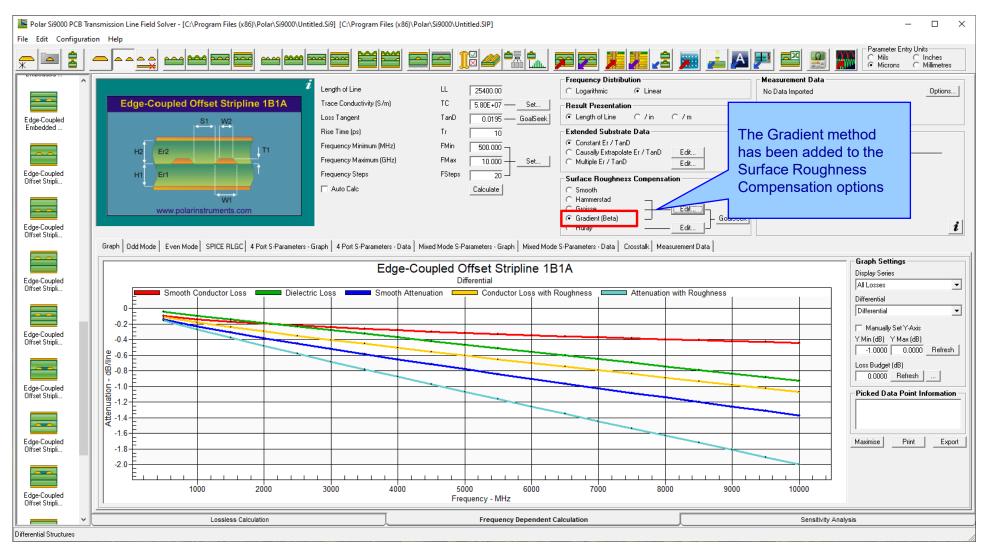


Si9000e v23.06.01 (June 2023)



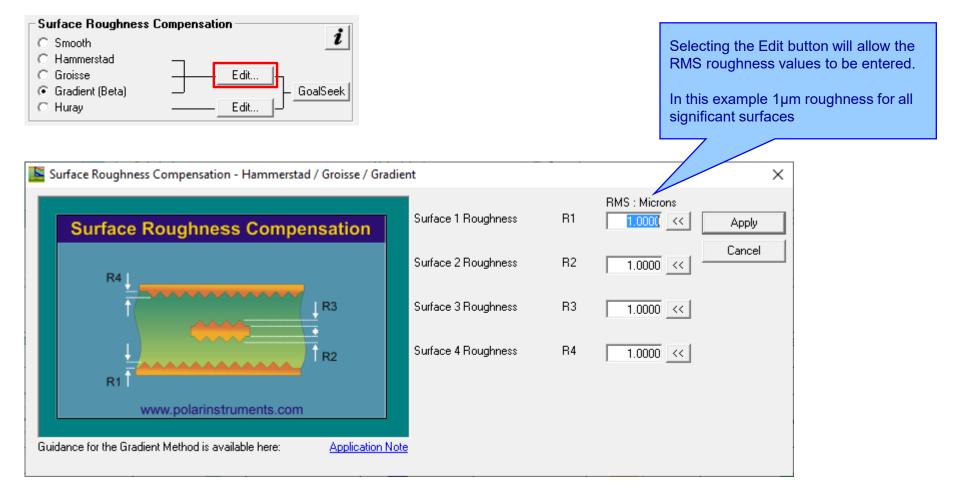
3

New Gradient Surface Roughness Compensation Method added





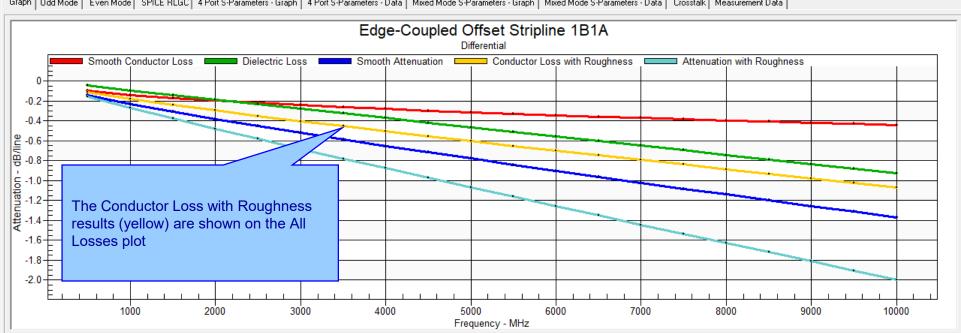
New Gradient Surface Roughness Compensation Method added



4



New Gradient Surface Roughness Compensation Method added



Graph Odd Mode Even Mode SPICE RLGC 4 Port S-Parameters - Graph 4 Port S-Parameters - Data Mixed Mode S-Parameters - Graph Mixed Mode S-Parameters - Data Crosstalk Measurement Data

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New Gradient Surface Roughness Compensation Method added

| Frequency | Impedance Real Ohms | Impedance Imaginary Ohms | Impedance Magnitude Ohms | Inductance H/line | Resistance Ohms/line | Capacitance F/line | Conductance S/line | Skin Depth m | Lonductor | Loss | Attenuation | Loss with Roughness | Attenuation with Roughness dB/line | Modal Phase Velocity m/s | Alpha Np/line | Alpha dB/line | Beta rad/line |
|-----------|---------------------------|--------------------------------|--------------------------------|----------------------|-------------------------|-----------------------|-----------------------|-----------------|------------|------------|-------------|------------------------|---|-----------------------------------|------------------|------------------|------------------|
| 5.000E+08 | 3.161E+01 | -3.664E-01 | 3.161E+01 | 5.955E-09 | 7.989E-01 | 5.964E-12 | 3.653E-04 | 2.955E-06 | -9.873E-02 | -4.717E-02 | | -1.128E-01 | -1.599E-01 | 1.348E+08 | 1.841E-02 | -1.599E-01 | 5.921E-0 |
| 1.000E+09 | 3.138E+01 | -2.268E-01 | 3.138E+01 | 5.869E-09 | 1.253E+00 | 5.964E-12 | 7.307E-04 | 2.090E-06 | -1.399E-01 | -9.376E-02 | -2.337E-01 | -1.792E-01 | -2.730E-01 | 1.358E+08 | 3.142E-02 | -2.730E-01 | 1.176E+0 |
| 1.500E+09 | 3.126E+01 | -1.675E-01 | 3.126E+01 | 5.827E-09 | 1.660E+00 | 5.964E-12 | 1.096E-03 | 1.706E-06 | -1.715E-01 | -1.403E-01 | -3.118E-01 | -2.391E-01 | -3.794E-01 | 1.363E+08 | 4.368E-02 | -3.794E-01 | 1.757E+0 |
| 2.000E+09 | 3.119E+01 | -1.330E-01 | 3.119E+01 | 5.799E-09 | 2.043E+00 | 5.964E-12 | 1.461E-03 | 1.478E-06 | -1.982E-01 | -1.867E-01 | -3.849E-01 | -2.957E-01 | -4.824E-01 | 1.366E+08 | 5.554E-02 | -4.824E-01 | 2.337E+0 |
| 2.500E+09 | 3.113E+01 | -1.097E-01 | 3.113E+01 | 5.779E-09 | 2.410E+00 | 5.964E-12 | 1.827E-03 | 1.322E-06 | -2.217E-01 | -2.331E-01 | -4.548E-01 | -3.501E-01 | -5.832E-01 | 1.368E+08 | 6.714E-02 | -5.832E-01 | 2.916E+0 |
| 3.000E+09 | 3.109E+01 | -9.260E-02 | 3.109E+01 | 5.763E-09 | 2.766E+00 | 5.964E-12 | 2.192E-03 | 1.207E-06 | -2.429E-01 | -2.795E-01 | -5.224E-01 | -4.028E-01 | -6.823E-01 | 1.370E+08 | 7.856E-02 | -6.823E-01 | 3.495E+0 |
| 3.500E+09 | 3.105E+01 | -7.937E-02 | 3.105E+01 | 5.750E-09 | 3.112E+00 | 5.964E-12 | 2.557E-03 | 1.117E-06 | -2.624E-01 | -3.259E-01 | -5.883E-01 | -4.543E-01 | -7.802E-01 | 1.372E+08 | 8.982E-02 | -7.802E-01 | 4.072E+0 |
| 4.000E+09 | 3.102E+01 | -6.871E-02 | 3.102E+01 | 5.739E-09 | 3.452E+00 | 5.964E-12 | 2.923E-03 | 1.045E-06 | -2.806E-01 | -3.723E-01 | -6.529E-01 | -5.047E-01 | -8.770E-01 | 1.373E+08 | 1.010E-01 | -8.770E-01 | 4.650E+0 |
| 4.500E+09 | 3.100E+01 | -5.988E-02 | 3.100E+01 | 5.730E-09 | 3.785E+00 | 5.964E-12 | 3.288E-03 | 9.851E-07 | -2.976E-01 | -4.187E-01 | -7.163E-01 | -5.543E-01 | -9.730E-01 | 1.374E+08 | 1.120E-01 | -9.730E-01 | 5.227E+0 |
| 5.000E+09 | 3.097E+01 | -5.241E-02 | 3.097E+01 | 5.721E-09 | 4.114E+00 | 5.964E-12 | 3.653E-03 | 9.346E-07 | -3.138E-01 | -4.650E-01 | -7.788E-01 | -6.032E-01 | -1.068E+00 | 1.375E+08 | 1.230E-01 | -1.068E+00 | 5.803E+0 |
| 5.500E+09 | 3.095E+01 | -4.597E-02 | 3.095E+01 | 5.714E-09 | 4.437E+00 | 5.964E-12 | 4.019E-03 | 8.911E-07 | -3.291E-01 | -5.114E-01 | -8.405E-01 | -6.515E-01 | -1.163E+00 | 1.376E+08 | 1.339E-01 | -1.163E+00 | 6.379E+0 |
| 6.000E+09 | 3.094E+01 | -4.034E-02 | 3.094E+01 | 5.707E-09 | 4.757E+00 | 5.964E-12 | 4.384E-03 | 8.532E-07 | -3.438E-01 | -5.577E-01 | -9.015E-01 | -6.992E-01 | -1.257E+00 | 1.377E+08 | 1.447E-01 | -1.257E+00 | 6.955E+0 |
| 6.500E+09 | 3.092E+01 | -3.536E-02 | 3.092E+01 | 5.702E-09 | 5.073E+00 | 5.964E-12 | 4.750E-03 | 8.197E-07 | -3.579E-01 | -6.040E-01 | -9.619E-01 | -7.464E-01 | -1.350E+00 | 1.377E+08 | 1.555E-01 | -1.350E+00 | 7.531E+0 |
| 7.000E+09 | 3.091E+01 | -3.091E-02 | 3.091E+01 | 5.696E-09 | 5.387E+00 | 5.964E-12 | 5.115E-03 | 7.899E-07 | -3.714E-01 | -6.503E-01 | -1.022E+00 | -7.931E-01 | . 3.4495.00 | | . 3. FOR M. | 1 442E200 | . 8,1005.0 |
| 7.500E+09 | 3.089E+01 | -2.691E-02 | 3.089E+01 | 5.691E-09 | 5.697E+00 | 5.964E-12 | 5.480E-03 | 7.631E-07 | -3.845E-01 | -6.967E-01 | -1.081E+00 | -8.395E-01 | 0 | opy Result | ts to Clipbo | pard (for F | xcel) |
| 8.000E+09 | 3.088E+01 | -2.327E-02 | 3.088E+01 | 5.686E-09 | 6.005E+00 | 5.964E-12 | 5.846E-03 | 7.389E-07 | -3.971E-01 | -7.430E-01 | -1.140E+00 | -8.855E-01 | | opy nesun | e to cripbi | | acci) |
| 8.500E+09 | 3.087E+01 | -1.995E-02 | 3.087E+01 | 5.682E-09 | 6.310E+00 | 5.964E-12 | 6.211E-03 | 7.168E-07 | -4.093E-01 | -7.893E-01 | -1.199E+00 | -9.311E-01 | -1.720E+00 | 1.380E+08 | 1.981E-01 | -1.720E+00 | 9.831E+0 |
| 9.000E+09 | 3.086E+01 | -1.690E-02 | 3.086E+01 | 5.678E-09 | 6.613E+00 | 5.964E-12 | 6.576E-03 | 6.966E-07 | -4.212E-01 | -8.356E-01 | -1.257E+00 | -9.764E-01 | -1.812E+00 | 1.380E+08 | 2.086E-01 | -1.812E+00 | 1.041E+0 |
| 9.500E+09 | 3.085E+01 | -1.409E-02 | 3.085E+01 | 5.674E-09 | 6.914E+00 | 5.964E-12 | 6.942E-03 | 6.780E-07 | -4.328E-01 | -8.819E-01 | -1.3155 0 | -1.021E+00 | -1.903E+00 | 1.381E+08 | 2.191E-01 | -1.903E+00 | 1.098E+0 |
| 1.000E+10 | 3.084E+01 | -1.148E-02 | 3.084E+01 | 5.671E-09 | 7.213E+00 | 5.964E-12 | 7.307E-03 | 6.609E-07 | -4.441E-01 | -9.282E-01 | +00 | -1.066E+00 | -1.994E+00 | 1.381E+08 | 2.296E-01 | -1.994E+00 | 1.155E+0 |

Graph Odd Mode Even Mode SPICE RLGC 4 Port S-Parameters - Graph 4 Port S-Parameters - Data Mixed Mode S-Parameters - Graph Mixed Mode S-Parameters - Data Crosstalk Measurement Data

The Conductor Loss with Roughness results data is also shown alongside the other field solver results.

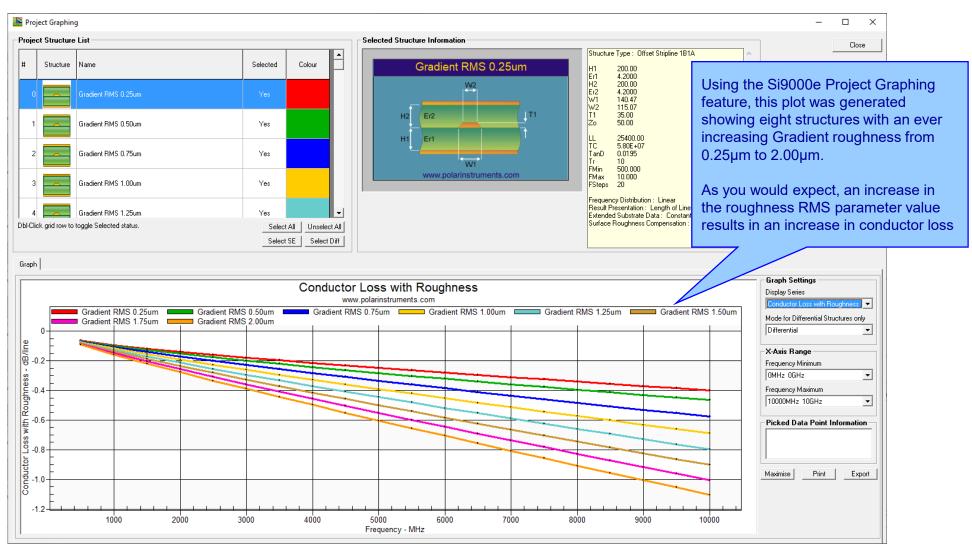
The complete set of results can be exported to third-party tools like Excel using the right-click menu | Copy Results to Clipboard

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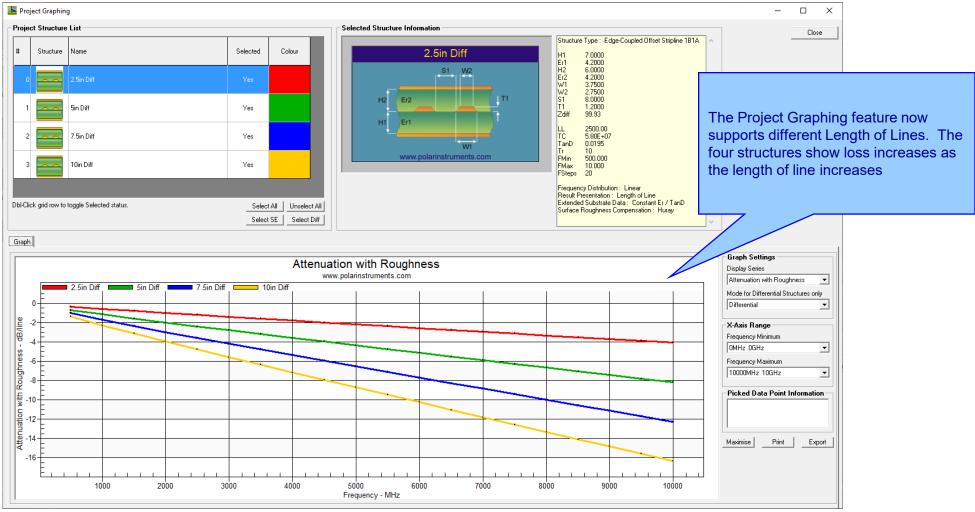
7

New Gradient Surface Roughness Compensation Method added





<u>Project Graphing Enhancements - now supports structures within</u> <u>the Project with varying Length of Line</u>





Other enhancements

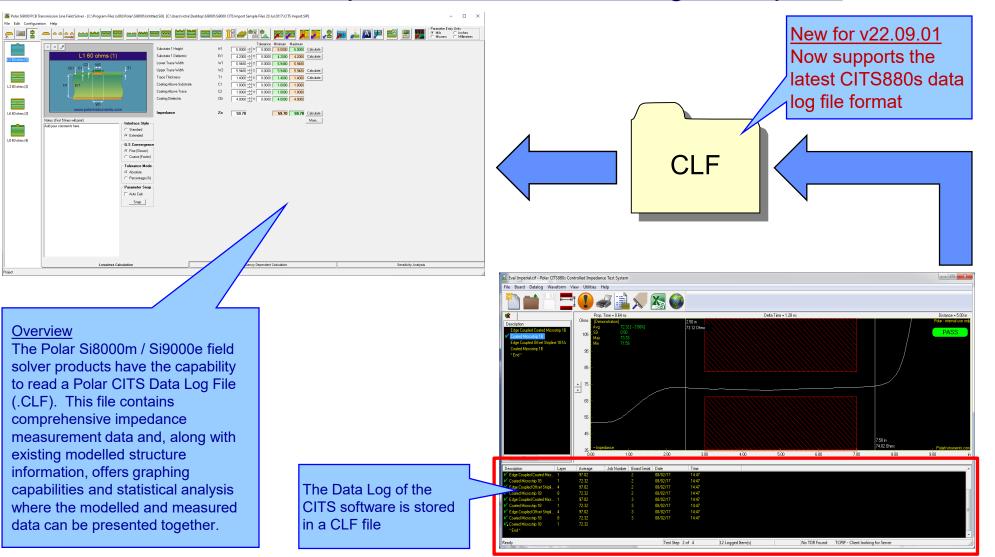
• FlexNet Publisher / FLEXIm v11.19.0.0 supported



Si9000e v22.09.01 (September 2022)



Enhancements to the Import Polar CITS Datalog File option



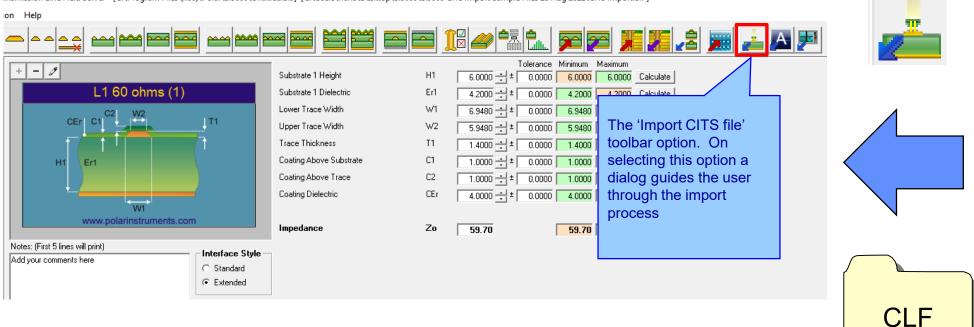
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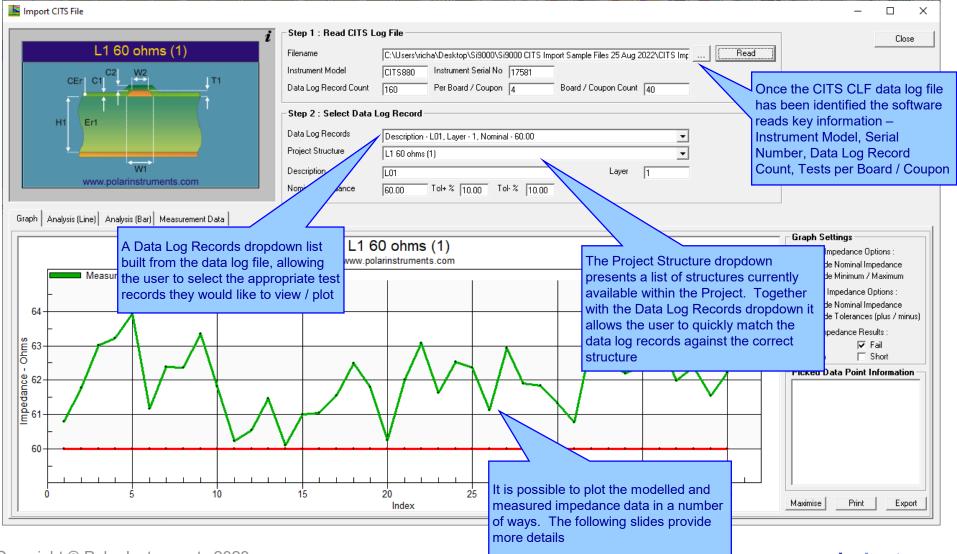
- Whilst working with controlled impedance designs it is often desirable to compare the reality of the measurement data against the modelled structure.
- 'Closing the loop' between the predicted and actual measured results has a number of benefits for both the design and fabrication environments. It allows for fine tuning of the structure parameters in future manufacturing batches, statistical analysis and improved overall process control.
- This capability within the Polar's Si8000m / Si9000e field solver products allows the user to quickly import measurement data directly from the industry-standard Polar Controlled Impedance Test System (CITS).
- If you are a design customer using the Si8000m / Si9000e and would like to use this feature, please request the Polar CITS Datalog File from your fabricator.





ansmission Line Field Solver - [C:\Program Files (x86)\Polar\Si9000\Untitled.Si9] [C:\Users\richa\Desktop\Si9000\Si9000 CITS Import Sample Files 25 Aug 2022\CITS Import.SIP]





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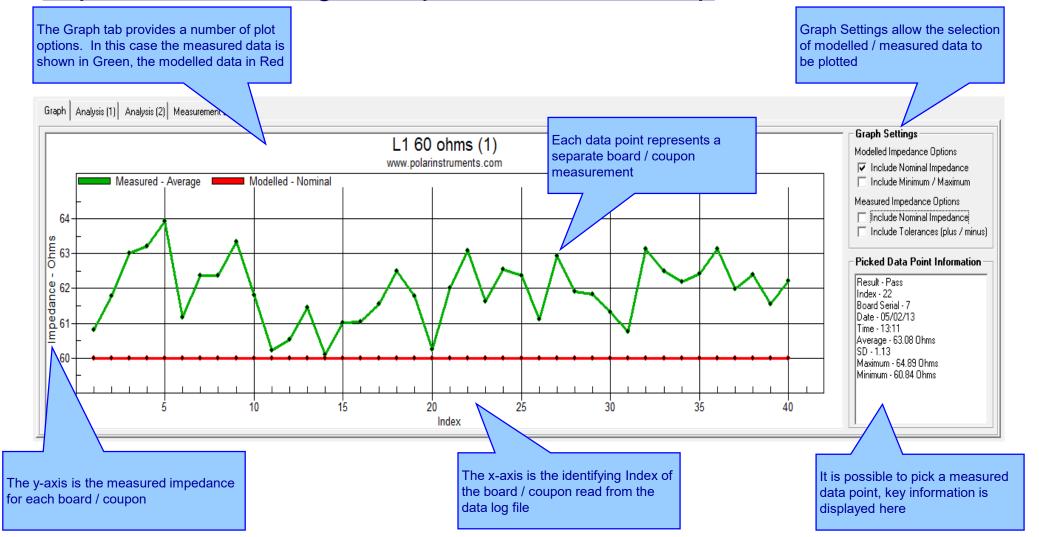
14



<u>Import CITS Datalog File option – feature recap</u>

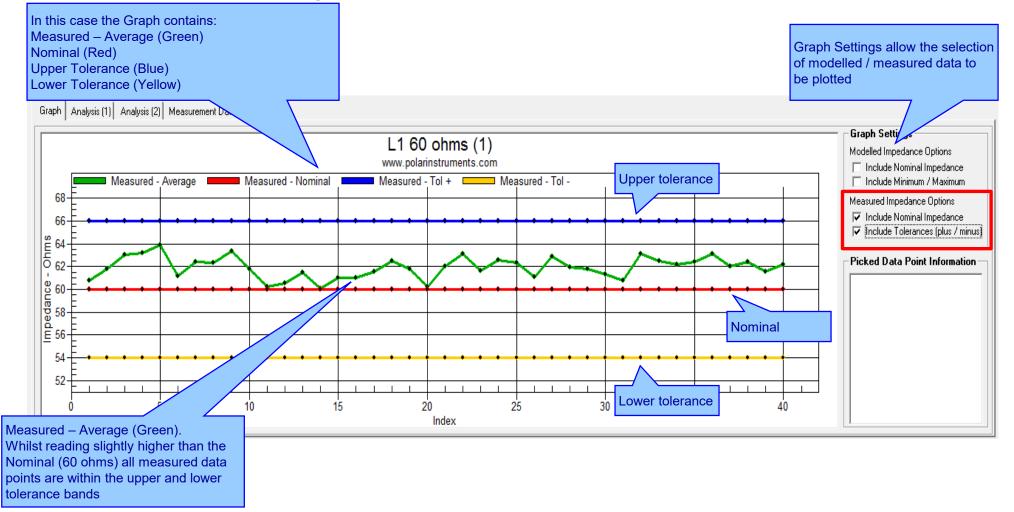
| Step 2 : Select Data | Log Record | Each test record type found in the data |
|-------------------------|--|---|
| Data Log Records | Description - L01, Layer - 1, Nominal Impedance - 60.00 | log file is listed in the drop down. In this |
| Project Structure | Description - L01, Laver - 1, Nominal Impedance - 60.00 | case there are four tests. |
| | Description - L03, Layer - 3, Nominal Impedance - 60.00 Description - L06, Layer - 6, Nominal Impedance - 60.00 | |
| Description | Description - L08, Layer - 8, Nominal Impedance - 60.00 | |
| Nominal Impedance | 60.00 Tol+ % 10.00 Tol- % 10.00 | Polar Si9000 PCB |
| | | File Edit Configu |
| -Step 2 : Select Data | Lag Pasard | |
| - Step 2 : Select Data | a Log necola | |
| Data Log Records | Description - L01, Layer - 1, Nominal Impedance - 60.00 | |
| Project Structure | L1 60 ohms (1) | |
| Description | L1 60 ohms (1) | L1 60 ohms (1) |
| | L3 60 ohms (2) L6 60 ohms (3) | |
| Nominal Impedance | L8 60 obms (4) | |
| | | |
| | | |
| tch one of the four m | nodelled | L3 60 ohms (2) |
| ures from the Project | group | |
| st a data log test reco | | |
| the structure from th | e Project | |
| ure dropdown | | |
| | | L6 60 ohms (3) |
| | | |
| | | |
| | | Four structures loaded into the Project group |
| | | |





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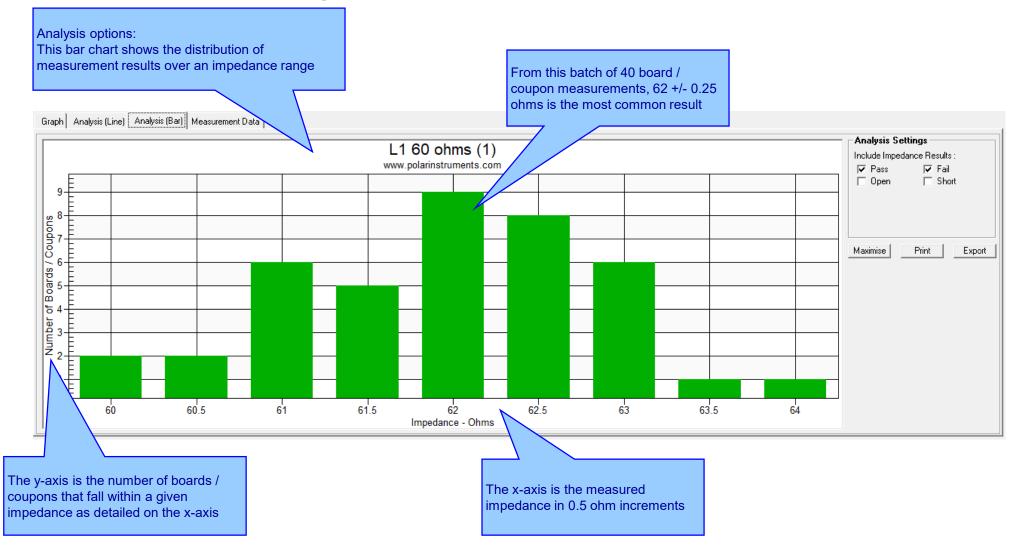


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Import CITS Datalog File option – feature recap





Measurement Data:

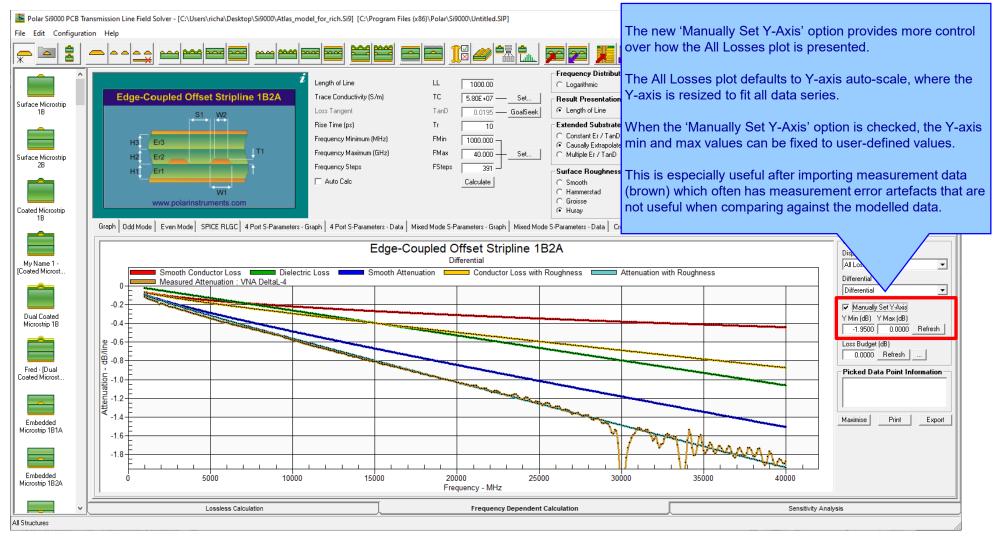
The CITS Data Log data may also be viewed in a data grid layout. This is especially useful for viewing the Result data (Pass / Fail)

Graph Analysis (Line) Analysis (Bar) Measurement Data

| Result | Index Board Serial | Date | Time | Average | SD | Maximum | Minimum 9 | Station | Description | Layer | Nominal | Tol+ % | Tol-% Instrument | Serial No | |
|--------|--------------------|-------------|-------|---------|------|---------|-----------|------------------|-------------|-------|---------|--------|------------------|-----------|-----|
| Passed | 1 | 24 05/02/13 | 12:48 | 60.8 | 0.8 | 61.9 | 59.56 | _TEST STATION 1_ | L01 | | 1 60 | 10 | 10 CITS880 | 17581 | |
| Passed | 2 | 29 05/02/13 | 12:50 | 61.77 | 0.95 | 63.21 | 59.93 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 3 | 17 05/02/13 | 12:51 | 63.01 | 0.94 | 64.48 | 61.68 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 4 | 39 05/02/13 | 12:52 | 63.22 | 1.07 | 64.62 | 61.29 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 5 | 8 05/02/13 | 12:59 | 63.93 | 0.95 | 65.32 | 62.2 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 6 | 10 05/02/13 | 13:00 | 61.17 | 0.89 | 62.69 | 59.63 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 7 | 32 05/02/13 | 13:01 | 62.38 | 0.88 | 63.58 | 60.72 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | (|
| Passed | 8 | 21 05/02/13 | 13:01 | 62.37 | 0.82 | 63.88 | 60.98 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | (|
| Passed | 9 | 4 05/02/13 | 13:02 | 63.35 | 0.68 | 64.41 | 61.75 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 10 | 33 05/02/13 | 13:03 | 61.81 | 0.78 | 62.95 | 60.09 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | l . |
| Passed | 11 | 18 05/02/13 | 13:03 | 60.22 | 0.62 | 61.48 | 59.09 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | l . |
| Passed | 12 | 3 05/02/13 | 13:04 | 60.54 | 0.75 | 62.1 | 59.19 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 13 | 15 05/02/13 | 13:05 | 61.46 | 0.73 | 62.83 | 60.12 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | (|
| Passed | 14 | 2 05/02/13 | 13:05 | 60.09 | 0.67 | 61.24 | 58.57 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | (|
| Passed | 15 | 23 05/02/13 | 13:06 | 61.01 | 0.78 | 62.4 | 59.69 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | (|
| Passed | 16 | 5 05/02/13 | 13:07 | 61.05 | 0.63 | 62.14 | 59.49 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | (|
| Passed | 17 | 6 05/02/13 | 13:07 | 61.54 | 0.8 | 62.98 | 60.11 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 18 | 76 05/02/13 | 13:08 | 62.49 | 0.92 | 63.44 | 60.32 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 19 | 11 05/02/13 | 13:09 | 61.79 | 0.83 | 63.08 | 60.37 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 20 | 31 05/02/13 | 13:09 | 60.25 | 0.65 | 61.37 | 58.85 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 21 | 12 05/02/13 | 13:10 | 62.01 | 0.69 | 63.24 | 60.65 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 22 | 7 05/02/13 | 13:11 | 63.08 | 1.13 | 64.89 | 60.84 | _TEST STATION 1_ | L01 | | 1 60 |) 10 | 10 CITS880 | 17581 | |
| Passed | 23 | 19 05/02/13 | 13:11 | 61.63 | 0.72 | 62 81 | 60.19 | TEST STATION 1 | 1.01 | | 1 60 | າ 10 | 10 CITS880 | 17581 | |



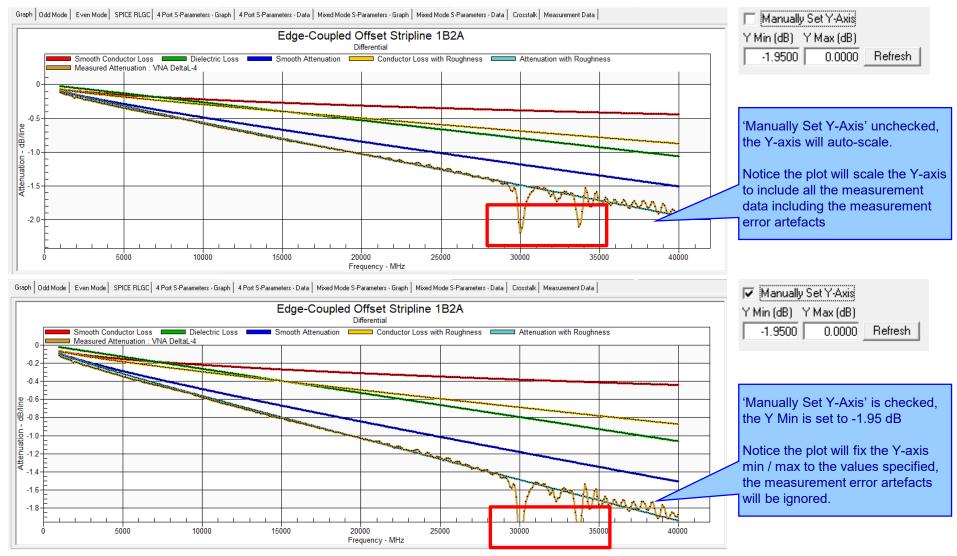
New Manually Set Y-Axis option for the All Losses plot



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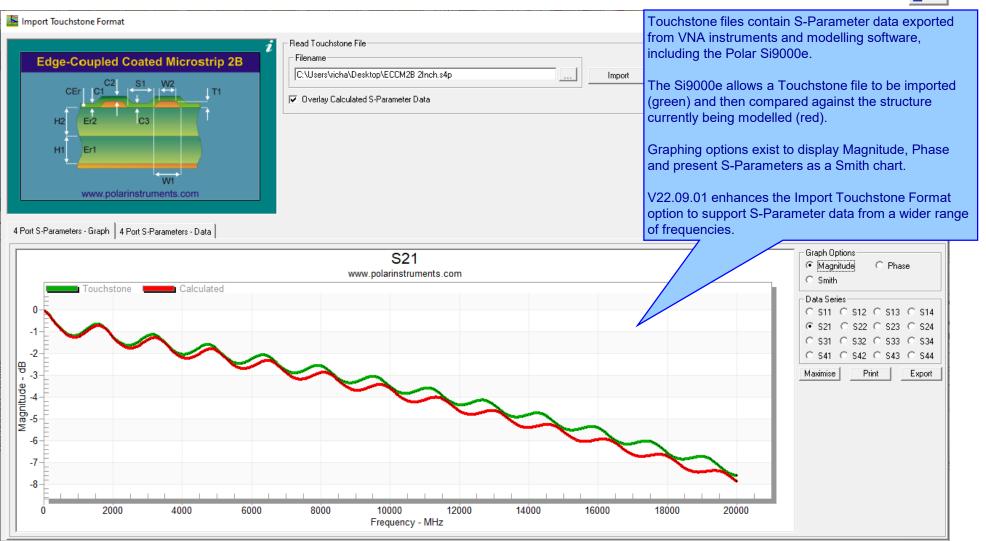
New Manually Set Y-Axis option for the All Losses plot





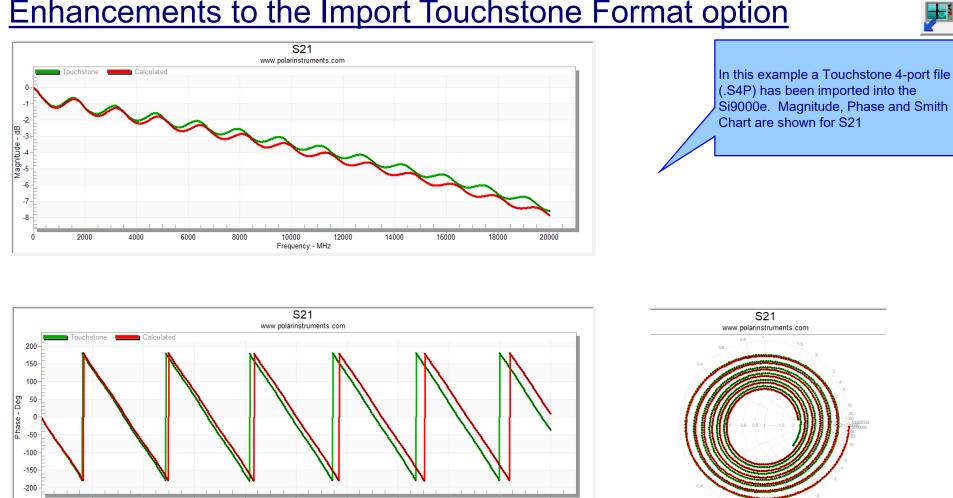
-5

Enhancements to the Import Touchstone Format option

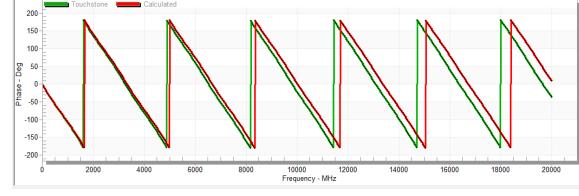


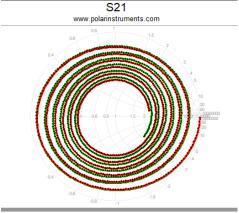
22





Enhancements to the Import Touchstone Format option



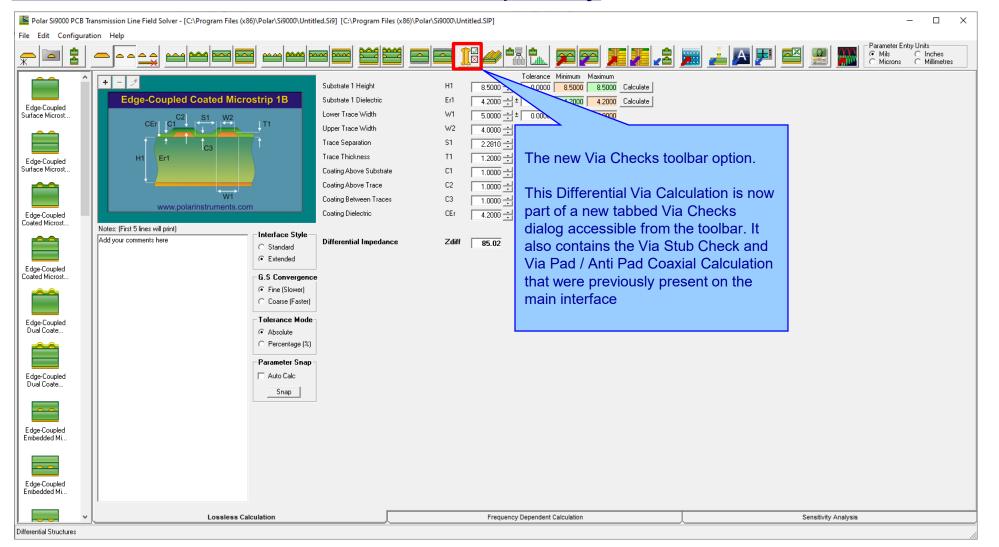




Si9000e v22.04 (April 2022)



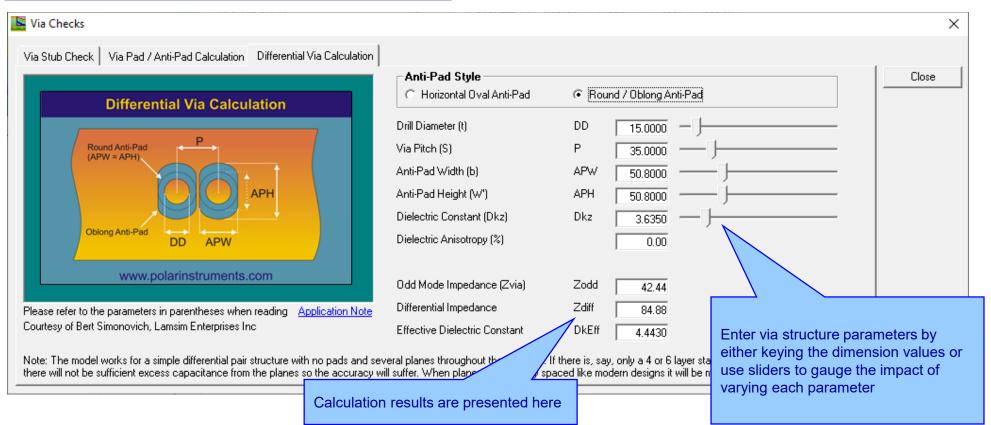
New Differential Via Calculation capability



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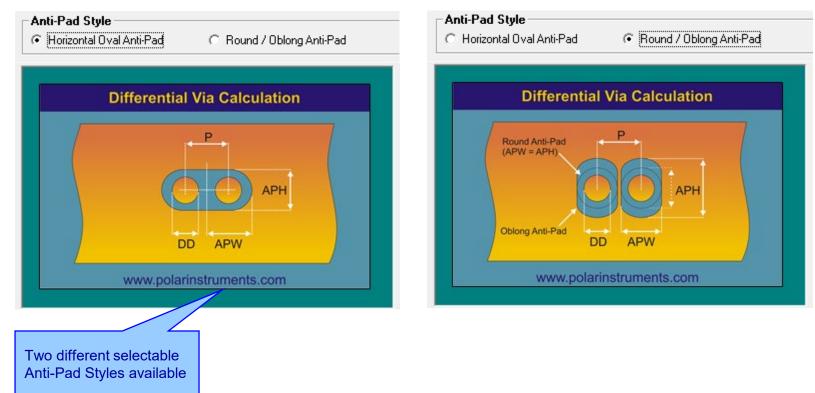


New Differential Via Calculation



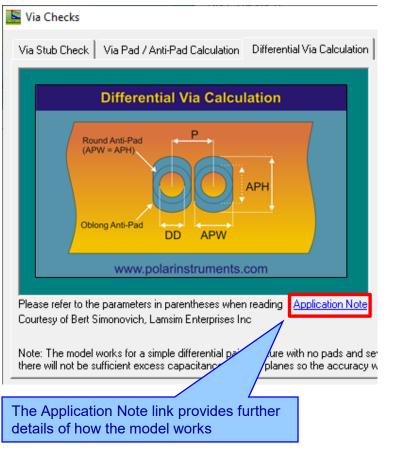


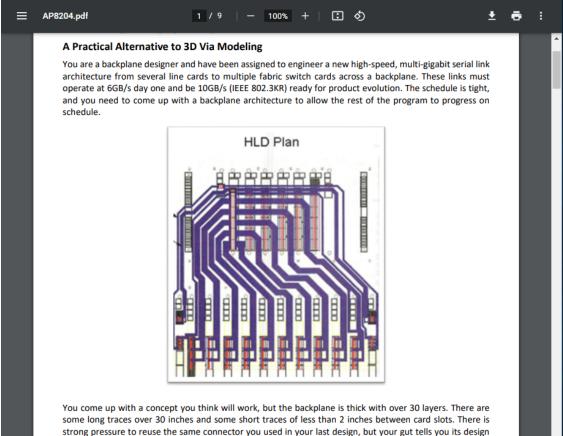
New Differential Via Calculation





New Differential Via Calculation





Finally, you are worried about the size and design of the differential via footprint used for the backplane connectors because you know they can be devastating to the quality of the received signal. You want to maximize the routing channel through the connector field, which requires you to shrink the anti-pad dimensions, so the tracks will be covered by the reference planes, but you can't easily quantify the consequences on the via of doing so.

may not be good enough for this higher speed application.

You have done all you can think of. based on experience. to make the vias as transparent as possible without



Si9000e v22.03 (March 2022)



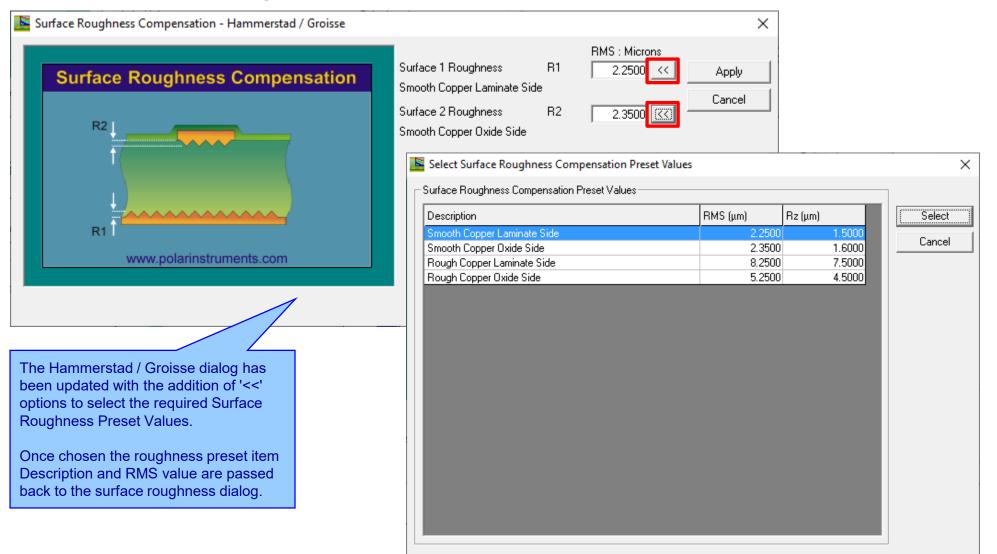
New Surface Roughness Compensation Preset Values option

| | Configuration Help | | | | |
|-----------------|---|--|------------------------|-------------------------------|----------|
| | Parameters Structures | | | | |
| | Loss Budgets | | | | |
| | Surface Roughness Compensation Preset Values | | | | |
| rface Mic 1B | Hatch Startup Mode | Surface Roughness Compensation Prese | t Values Configuration | | |
| IB | Si Excel Interface | | | | |
| | Track Resistance Calculator (TRC) | Surface Roughness Compensation Preset Valu | les | | |
| | Graph Style | Description | RMS (μm) Rz (μm) | Add Entry | (|
| ace Mic 2B | Solver Accuracy | | | | Apply |
| _ | Save Current Parameter Settings as Defaults | Smooth Copper Laminate Side | 2.2500 | 1.5000 1 coop Delete Entry | v Cancel |
| | License Options | Smooth Copper Oxide Side | 2.3500 | 1.6000 | |
| ted Mic | · · · · | Rough Copper Laminate Side | 8.2500 | 7.5000 Edit Entry | |
| 1B | Language Settings | Rough Copper Oxide Side | 5.2500 | 4.5000 | |
| | | | | | |
| onfig urfac | entry has been added to the uration menu to manage a tal e Roughness Preset Values used of Description, RMS and ues | | | | |



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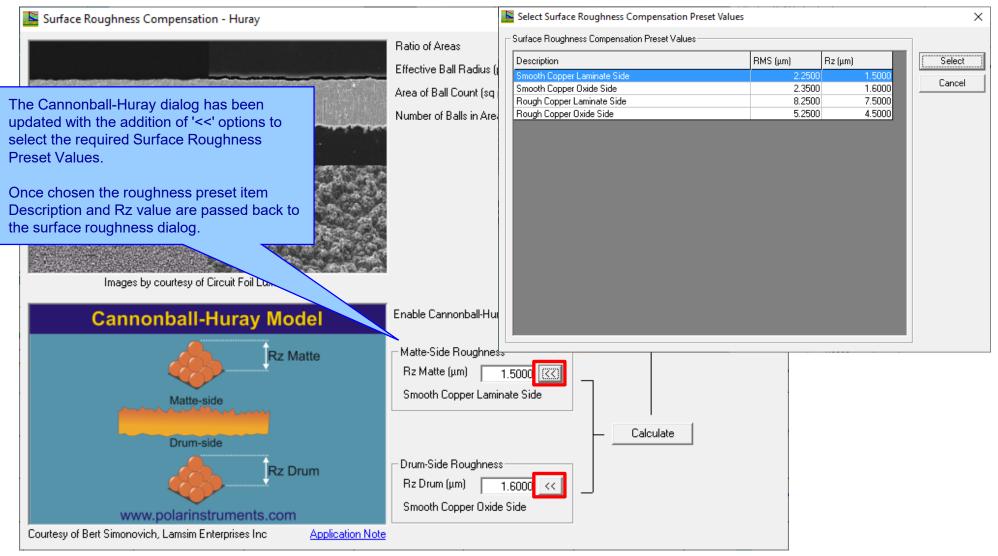
New Surface Roughness Compensation Preset Values option





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New Surface Roughness Compensation Preset Values option

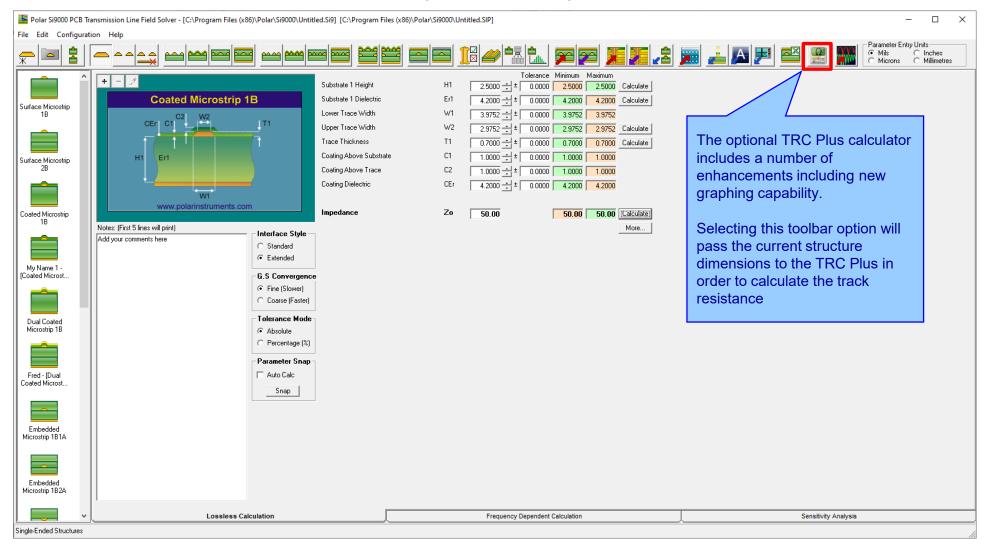




Si9000e v22.02 (February 2022)



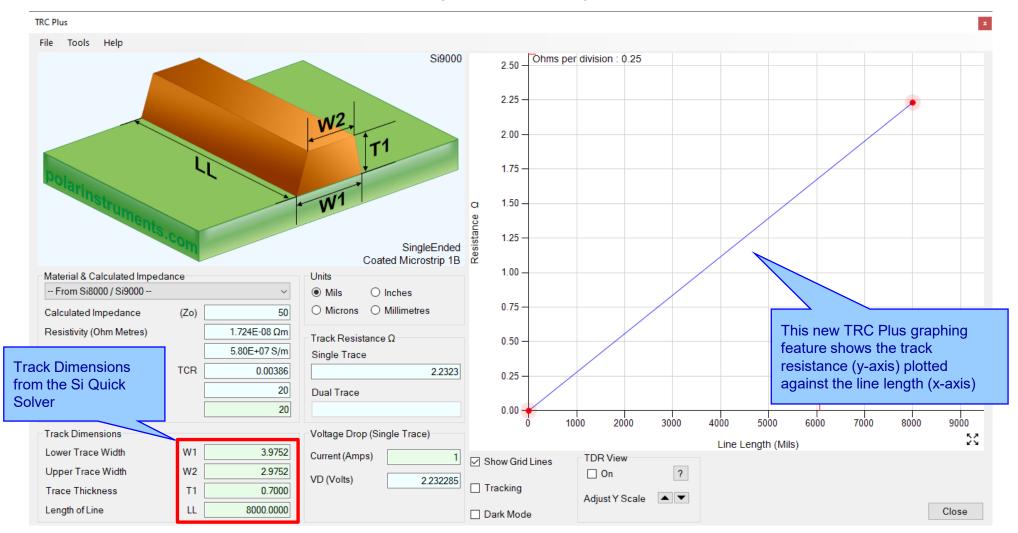
Track Resistance Calculator (TRC Plus) enhancements



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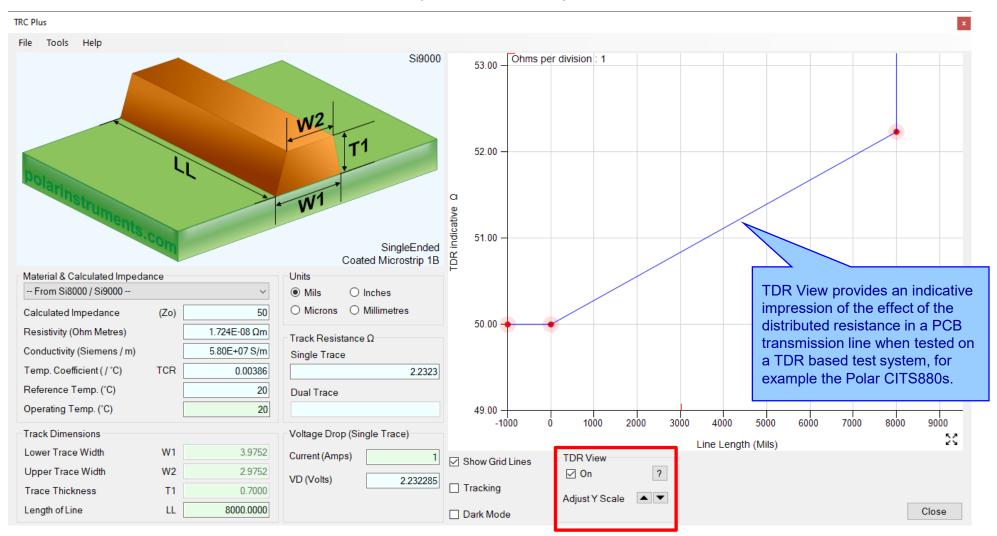
Track Resistance Calculator (TRC Plus) enhancements



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Track Resistance Calculator (TRC Plus) enhancements



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Si9000e v21.09 (Sept 2021)



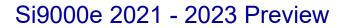
Project Graphing – Introduction (requires the Si Projects feature)

It is often useful to compare the results from similar structures, especially with frequency dependent calculations where changing just one or two parameters can have significant impact.

Until now the Si9000e Quick Solver graphing has focused on a single structure, for instance the All Losses graph will display a single plot that includes multiple data series for the same structure.

The new Project Graphing option calculates all the results for a group of structures contained in the Project and then plots the selected data series (total attenuation, conductor loss or dielectric loss etc) on the same graph.

A single graph that combines results from multiple structures is useful in a number of ways. Comparing the impact of different dielectric materials, different roughness, sensitivity analysis for lossy calculations and many more uses.

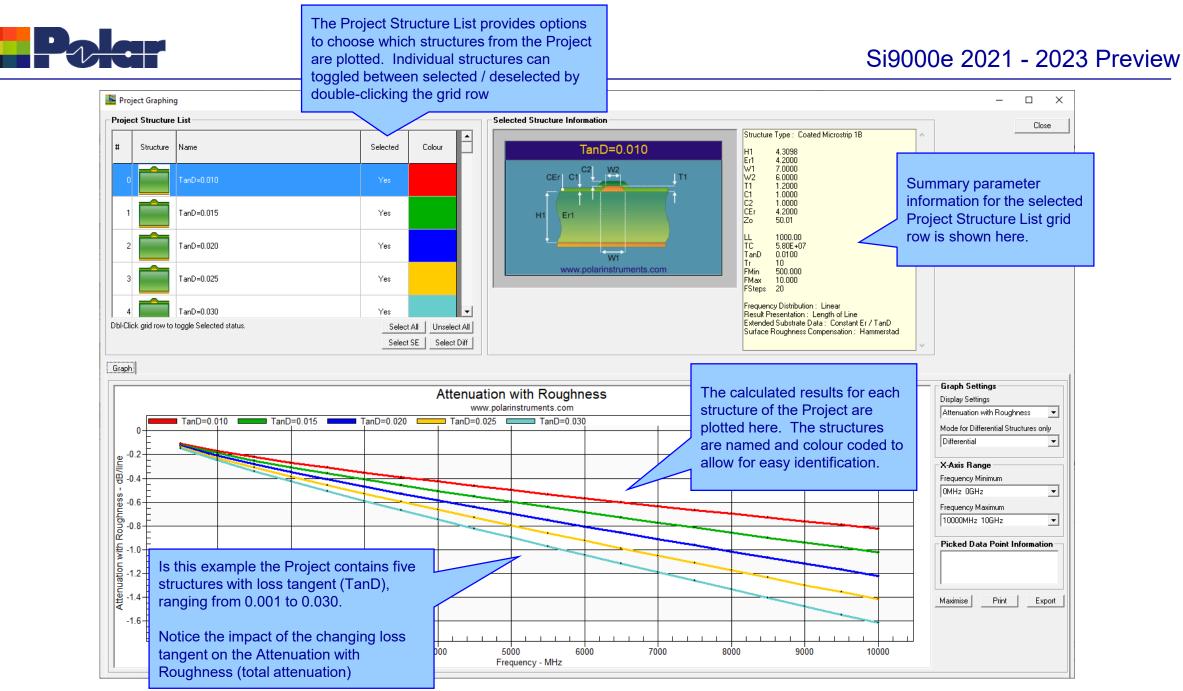




Project Graphing



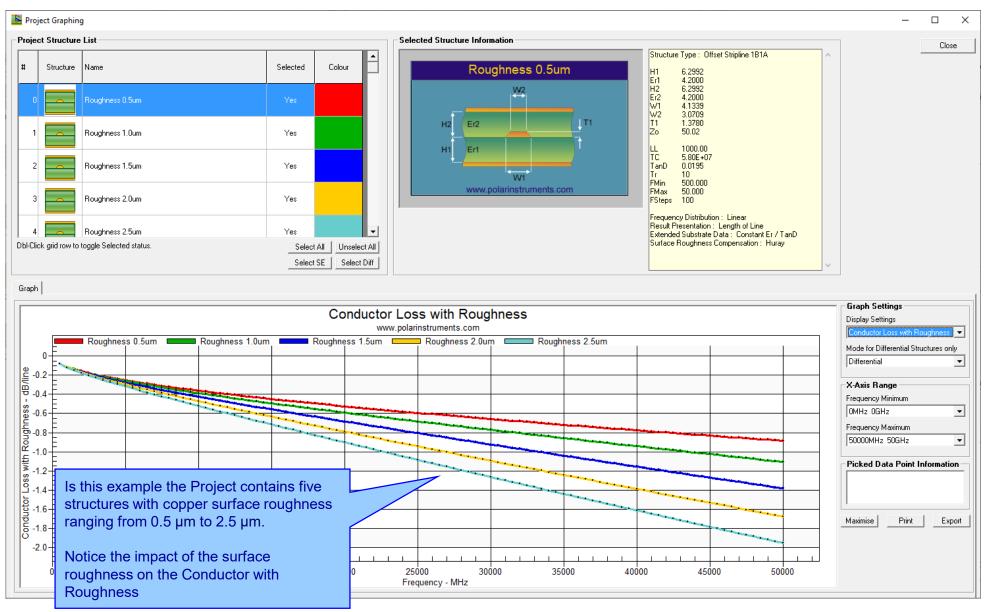
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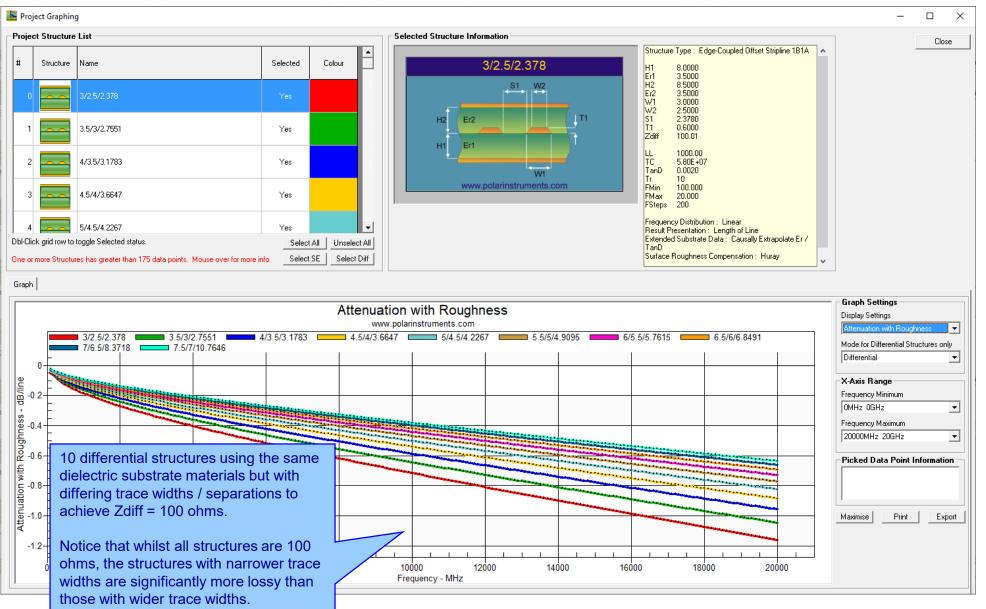
Si9000e 2021 - 2023 Preview



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Project Graphing – Summary

- The new Graphing option for Si Projects provides useful plots that contain data from multiple structures
- There are numerous uses for this type of option comparing the impact of different dielectric materials, different roughness, sensitivity analysis for lossy calculations and more
- 'What if' scenarios where one structure in the project would use the current design parameters and the second structure would contain a modified set based on a newer material. The plots comparing the original versus the new material will instantly show the impact
- Useful to both fabricators and design companies

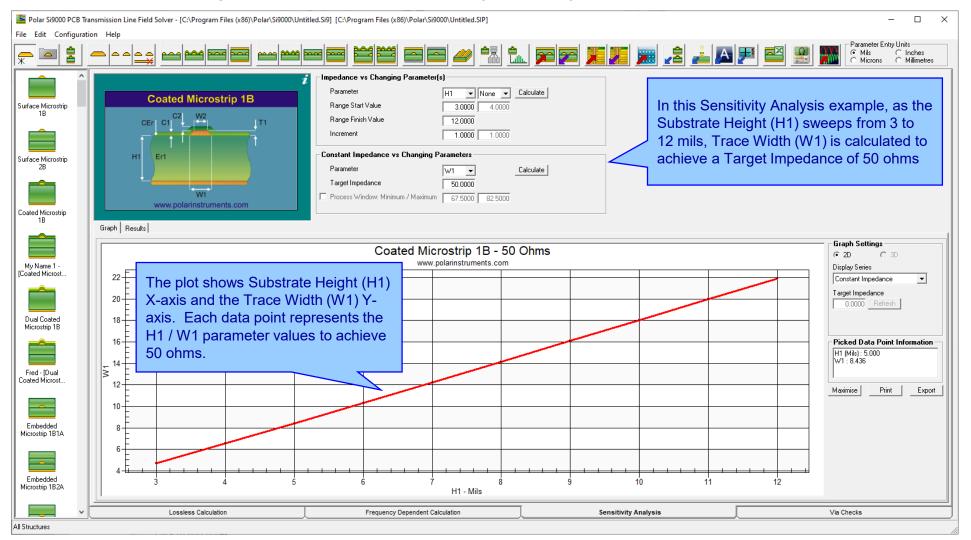


(requires the Si Projects feature)

When using the Sensitivity Analysis option it is often useful to examine the calculated results in more details. It is now possible to auto-create a Project containing structures based upon the Sensitivity Analysis results data.

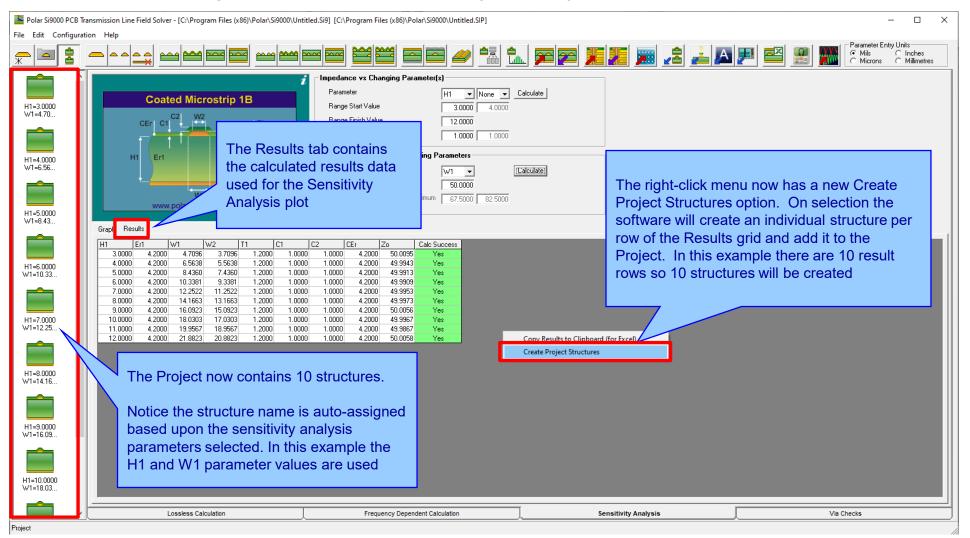
The following slides provide further details:



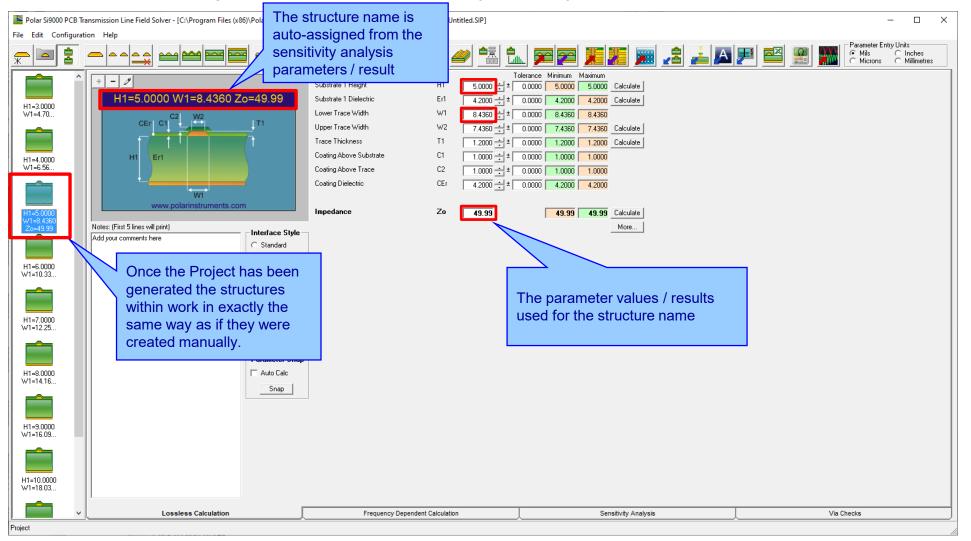


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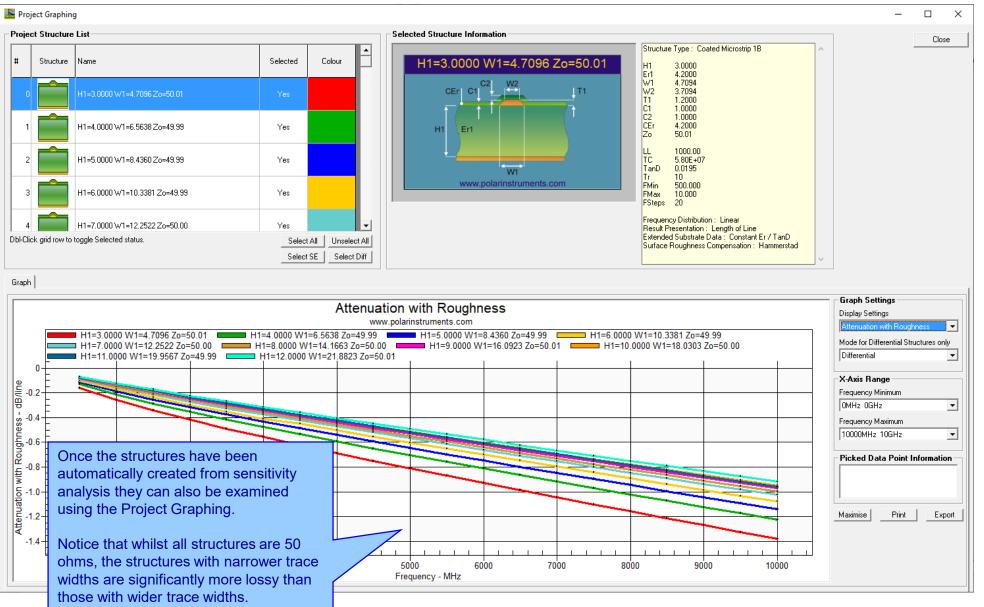




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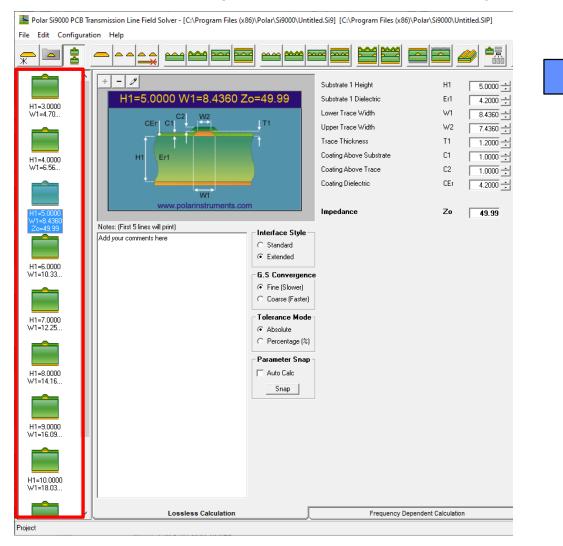
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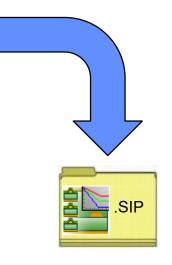


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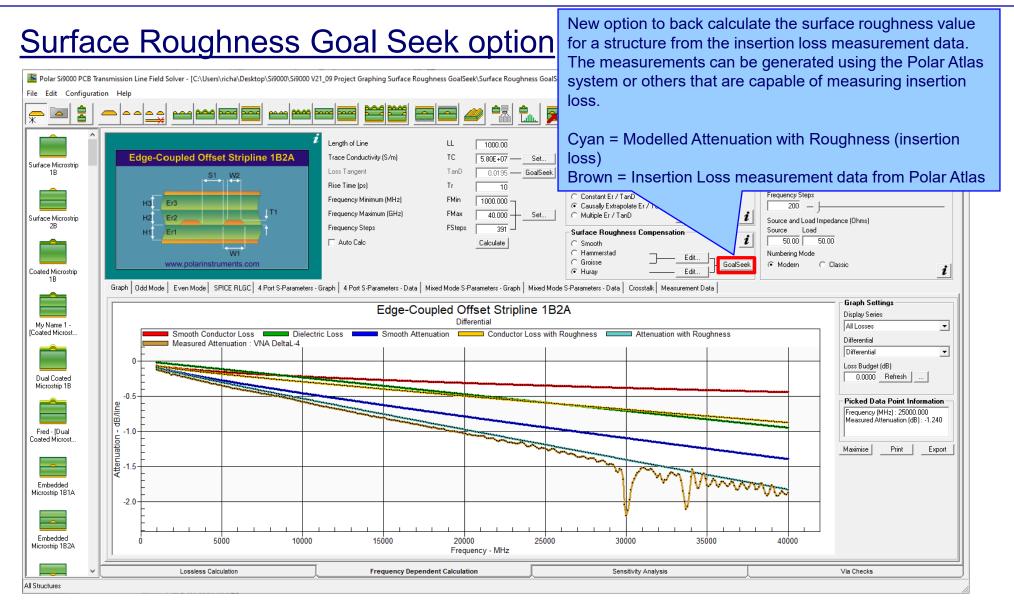
Save the newly created project to the Si Project file format (.SIP) so that it can be recalled at a later date.

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- As separate structure in a Project it is now possible to examine the results in a lot more detail than when in sensitivity analysis
- Lossy calculations can be performed and compared
- As a Project the structure data can be stored as a .SIP file and recalled later
- Useful to both fabricators and design companies





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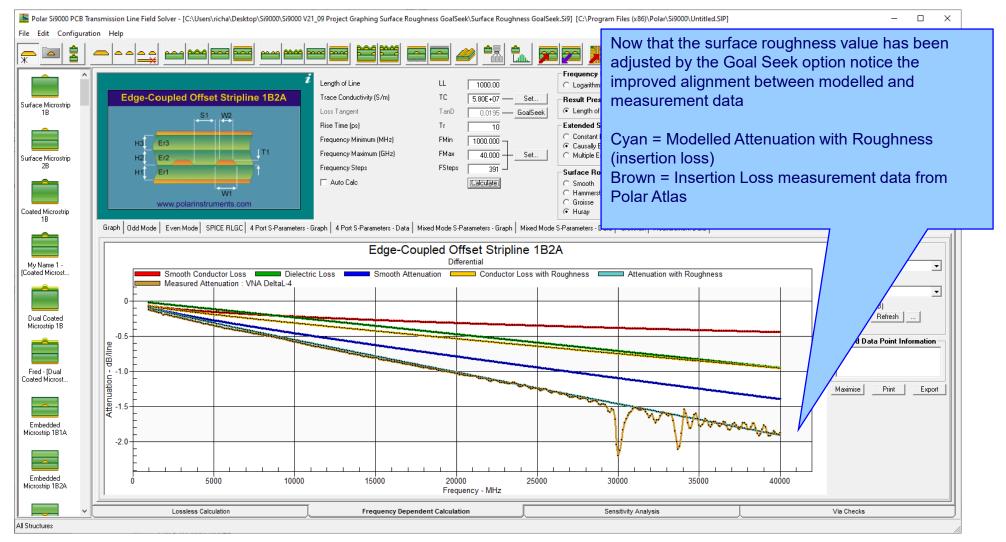


Surface Roughness Goal Seek option

| 📕 Surface Roughness Goal Seek | × | |
|---|-------|--|
| Step 1 : Enter Total Attenuation from measurement Freq (Hz) dB / LL Total Attenuation (S21 / SDD21) 2.50E+10 -1.2400 | Close | <u>Step 1</u> Key in or pick the total attenuation (S21 / SDD21) at a given frequency from the insertion loss measurement data |
| Step 2 : Calculate Dielectric and Conductor Loss dB / LL Dielectric Loss Conductor Loss with Roughness (Total Attenuation - Dielectric Loss) Step 3 : Calculate Surface Roughness | | Step 2 Calculate the dielectric loss for the frequency entered from the current structure parameters. Subtracting this calculated dielectric loss from the total attenuation will leave the target conductor loss |
| Cannonball-Huray Rz (μm) 2.2729 Calculate) Surface Roughness: 2.2729 Conductor Loss with Roughness: -0.6451 Setup Goal Seek Parameters Cannonball-Huray Rz (μm) Min Max < T1/2 | i | Step 3 Use the Si9000 Goal Seek algorithm to vary the surface roughness until it matches the required value to achieve the conductor loss as calculated in Step 2. In this example a Surface Roughness of 2.2729 μm is required |



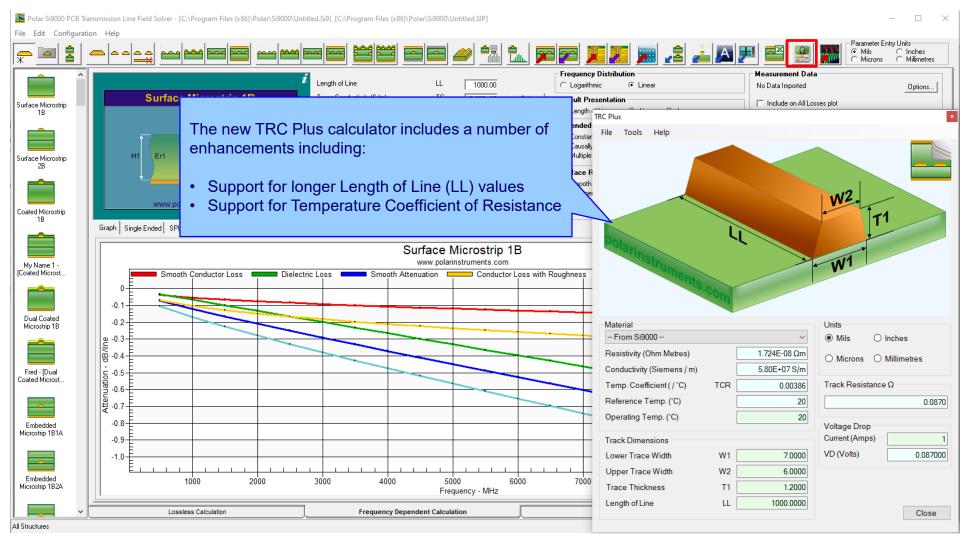
Surface Roughness Goal Seek option



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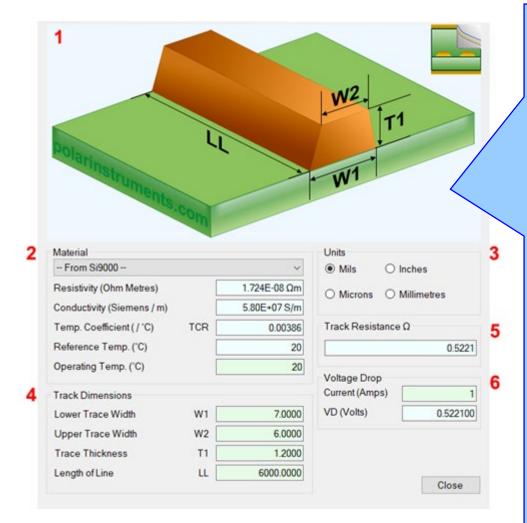


Track Resistance Calculator (TRC Plus)





Track Resistance Calculator (TRC Plus)



1. Interactive track material image.

Clicking on a track parameter label will highlight the associated Track Dimension field (text box). Enter data into the active field.

Double-clicking anywhere on the image will bring up the Materials Editor.

2. Material selection and properties Select the material via the drop-down list.

Fields coloured in light-blue are not directly editable but the field values can be in the Materials Editor.

Fields coloured in light-green are editable by the user. For example, Operating Temperature will determine a material's resistivity at that temperature, which in turn will be applied in calculating the track resistance.

3. Units

Switch to your preferred units by clicking the associated option button – imperial units include Mils (Thou) and Inches; for metric units choose Microns (Micrometres) or Millimetres.

4. Track or trace dimensions

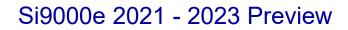
Enter or change track dimensions in the Track Dimensions in the chosen units.

5. Resistance result

Calculation of the track resistance. The result should update immediately upon any changes to the editable (light-green) fields.

6. Voltage Drop calculation result

The calculated Voltage Drop is displayed in the VD (Volts) text box





Other enhancements

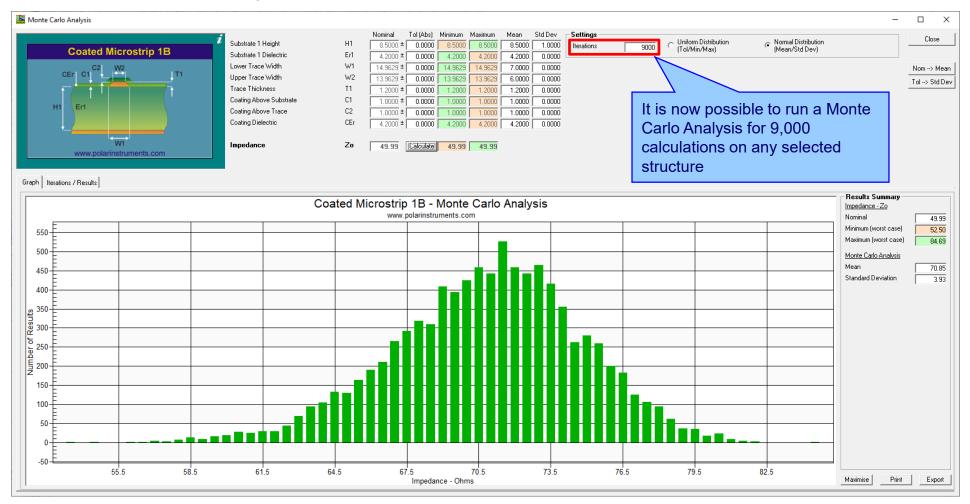
- Monte Carlo Analysis. New option added to export the Iterations / Results to Clipboard (for Excel), accessible from the right-click menu
- Causally Extrapolated Substrate Data. New option added to export the Results to Clipboard (for Excel), accessible from the right-click menu



Si9000e v21.04 (April 2021)



Monte Carlo Analysis maximum iteration increased to 9000



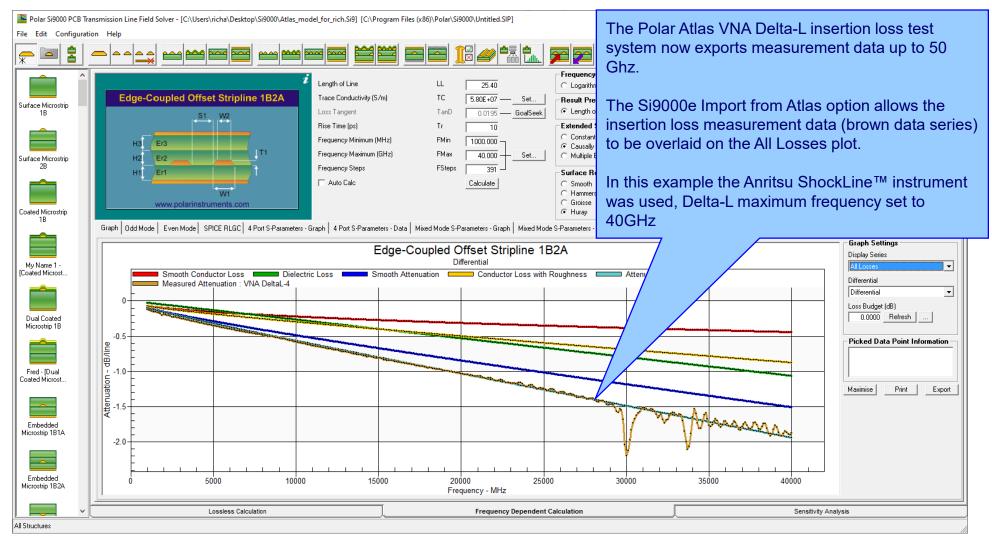
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Import from Atlas enhanced to support measurement data to 50GHz

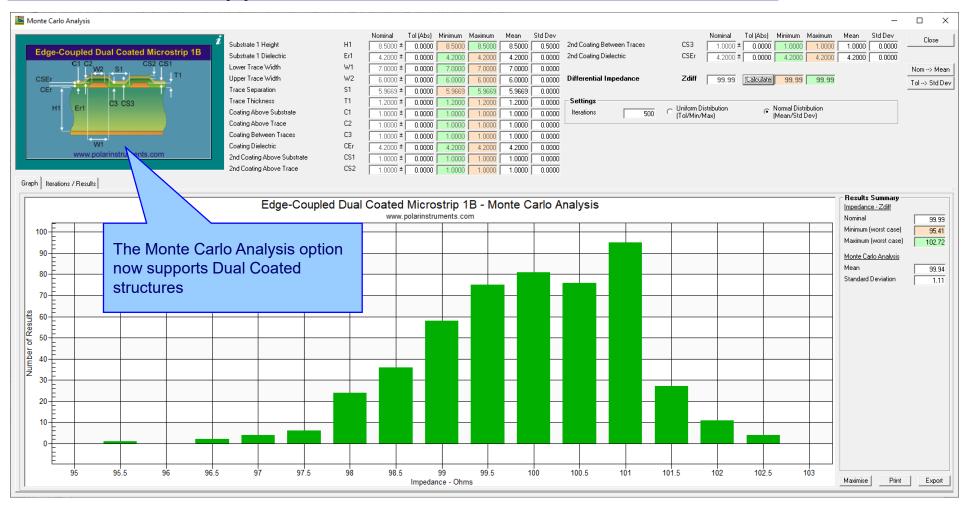




Si9000e v21.01 (January 2021)



Monte Carlo support added for Dual Coated structures

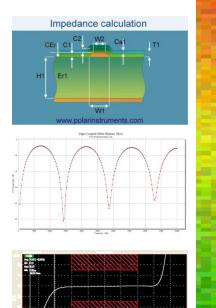


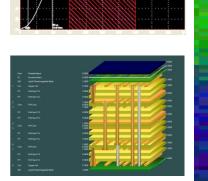


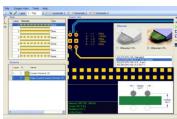
Other enhancements

• FlexNet Publisher / FLEXIm v11.17.2.0 supported









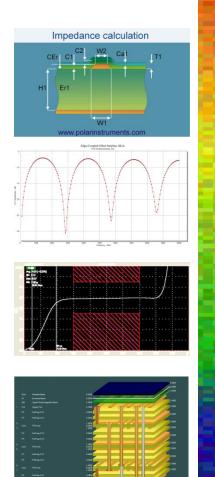
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| Coated Microstrip 1B | Edge-Coupled Offset Stripline 1B1A1R | Surface Coplanar Strips With Ground 2B | Coated Coplanar Strips 2B | Diff Coated Coplanar Waveguide 2B | Dual Coated Microstrip 1B | Edge-Coupled Offset Stripline 2B1A1R |
|----------------------|--|--|---------------------------|-----------------------------------|---------------------------|---|
| | HC EC IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII | H2 E2 T C1 | DI CEC CI CU VU DI CA TI | | | 51 W2 H5 E3 171 H5 E2 H1 E1 W1 Www.polarist.umers.com |

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