Si8000m / Si9000e / Speedstack 2015 Projects feature
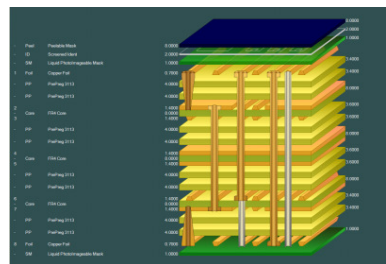
Richard Attrill / John Lee

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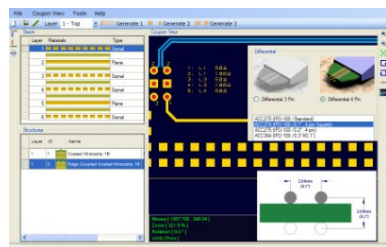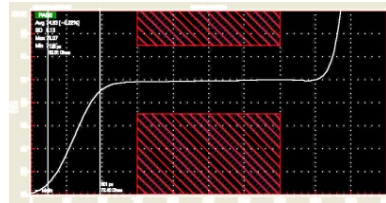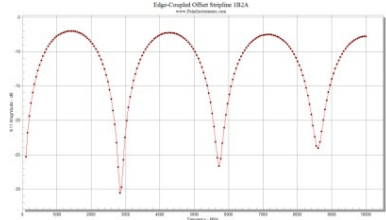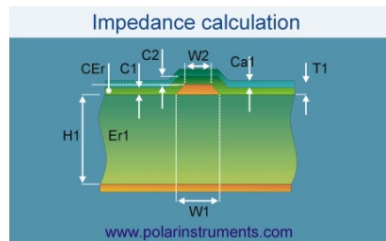
Introducing the Si8000m / Si9000e & Speedstack Projects feature

Key benefits:

- Group together a set of related structures for a particular design

- Multiple instances of the same structure type with different parameter values

- Maintain simplicity of operation

- Existing functionality remains, enhanced with project functions

- Import a complete set of Speedstack structures in a single step

- Available for both Si8000m and Si9000e

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2015 Preview

New 'Project' group

The Project group will allow multiple structures of the same type, allowing the user to quickly build a list of structures associated with a particular design. In this case layer L1 has a two coated microstrips, one for 50 ohms, one for 75 ohms. Selecting the structure thumbnail from Project group will load the details into the main Si interface.

New right-click menu will allow structures in the Project group to be added, deleted or edited

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Continue the process of adding until all structures required for the design are in the Project

Right-click menu on Project group provides options to edit structures

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Rename structure

Move up / down

Duplicate structure

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In addition to creating project groups within Si8000m / Si9000e, you can also import a group of structures from Speedstack.

This will allow all structures for a stack to be further analysed for loss / sensitivity analysis

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New Copy / Paste structure parameter options

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A new Si Project file format (.SIP) contains all the info in the Project group. New options to Open and Save the Project group have been created and placed on the File menu.

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New Copy / Paste structure parameter options

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Surface Microstrip 1B

		Tolerance	Minimum	Maximum	
Substrate 1 Height	H1	8.5000 ± 0.0000	8.5000	8.5000	Calculate
Substrate 1 Dielectric	Er1	4.2000 ± 0.0000	4.2000	4.2000	Calculate
Lower Trace Width	W1	6.0000 ± 0.0000	6.0000	6.0000	Calculate
Upper Trace Width	W2	5.0000 ± 0.0000	5.0000	5.0000	Calculate
Trace Thickness	T1	1.2000 ± 0.0000	1.2000	1.2000	Calculate
Impedance	Zo	80.03	80.03	80.03	

Often it is useful to pass parameters between structures. In this case the parameters will be copied from Surface Microstrip 1B

Coated Microstrip 1B
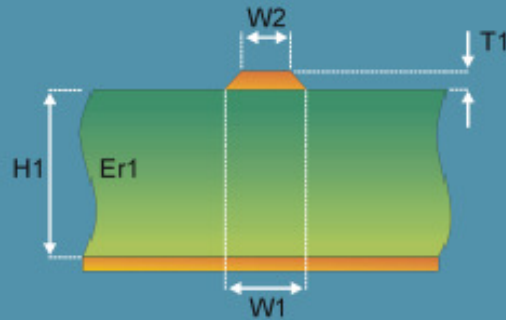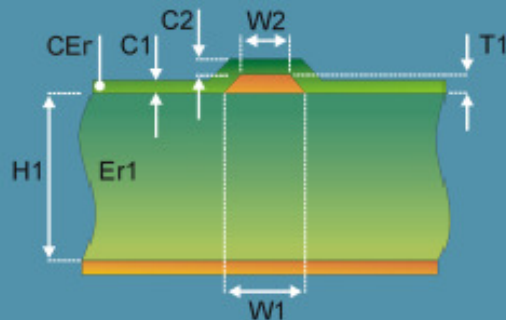
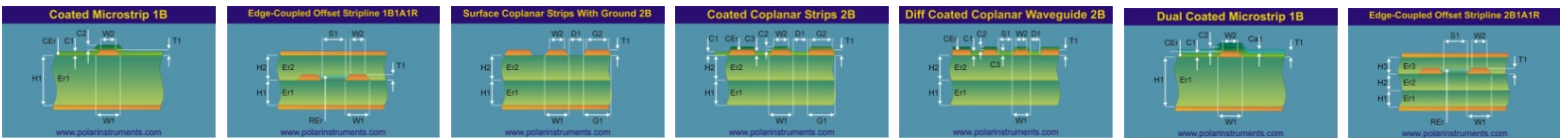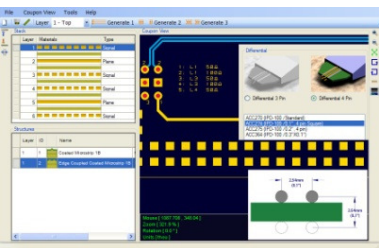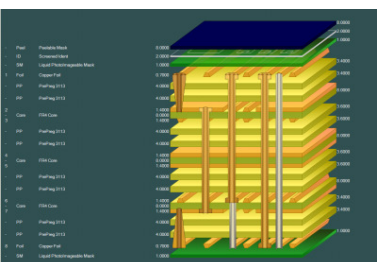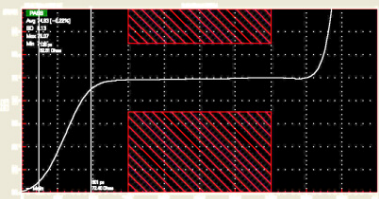|		Tolerance	Minimum	Maximum	
Substrate 1 Height	H1	8.5000 ± 0.0000	8.5000	8.5000	Calculate
Substrate 1 Dielectric	Er1	4.2000 ± 0.0000	4.2000	4.2000	Calculate
Lower Trace Width	W1	6.0000 ± 0.0000	6.0000	6.0000	
Upper Trace Width	W2	5.0000 ± 0.0000	5.0000	5.0000	Calculate
Trace Thickness	T1	1.2000 ± 0.0000	1.2000	1.2000	Calculate
Coating Above Substrate	C1	1.0000 ± 0.0000	1.0000	1.0000	
Coating Above Trace	C2	1.0000 ± 0.0000	1.0000	1.0000	
Coating Dielectric	CEr	4.2000 ± 0.0000	4.2000	4.2000	
Impedance	Zo	75.28	75.28	75.28	

… and then pasted to Coated Microstrip 1B. This should reduce the need to note down the parameter values before keying them in on the new structure
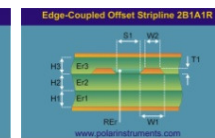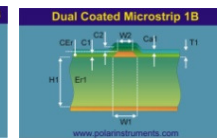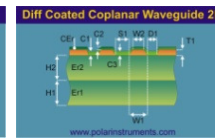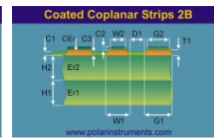
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Thank you

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Impedance calculation

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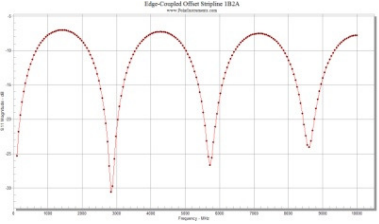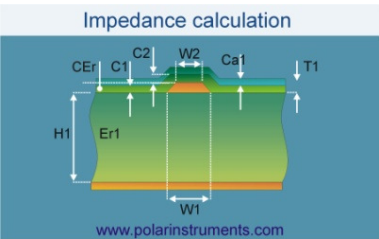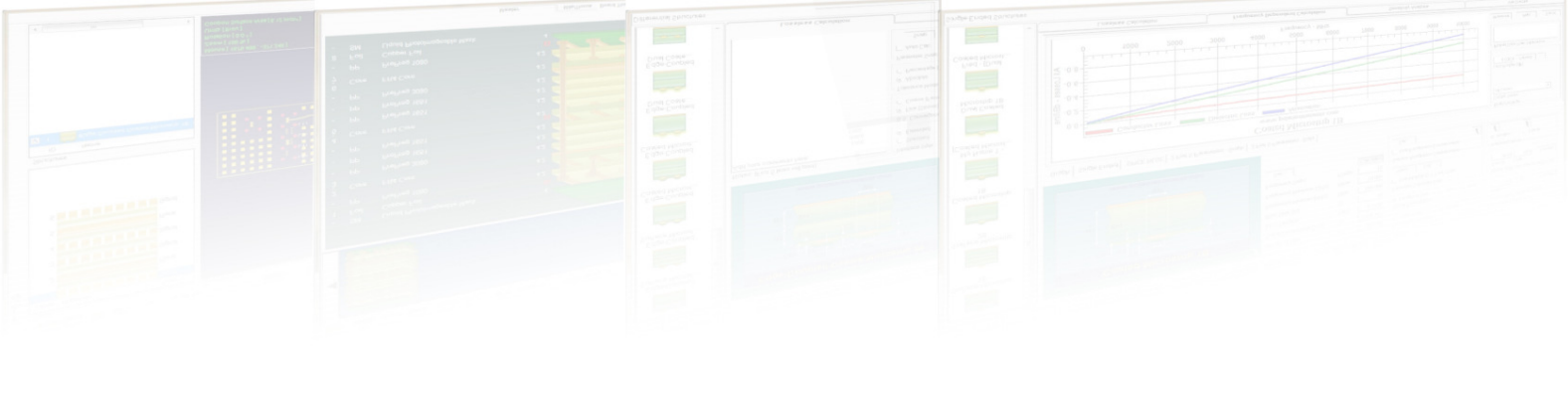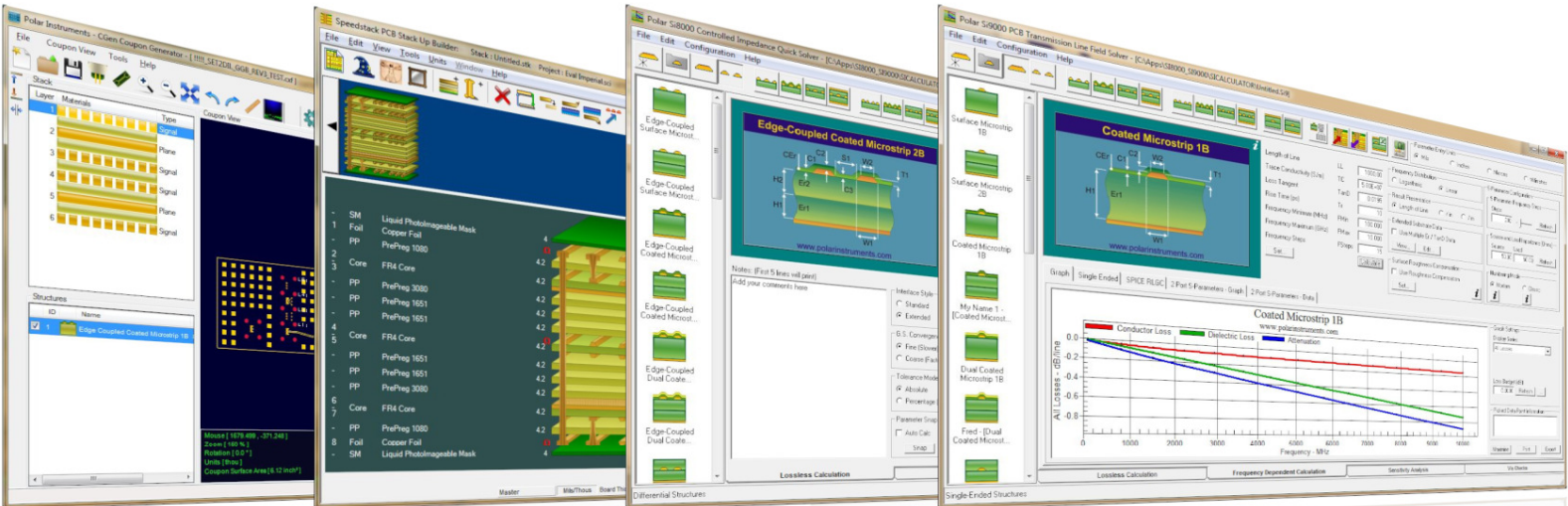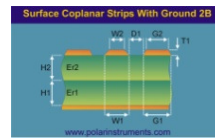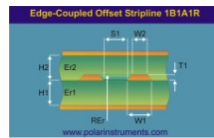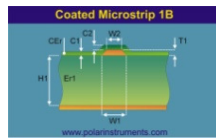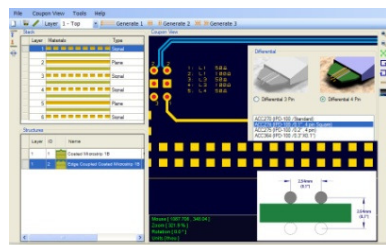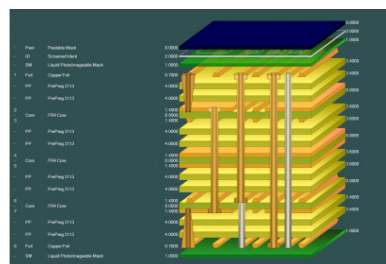
For more information:

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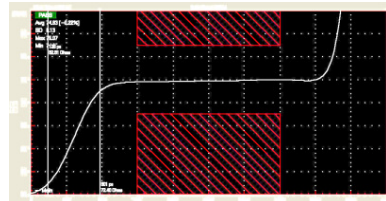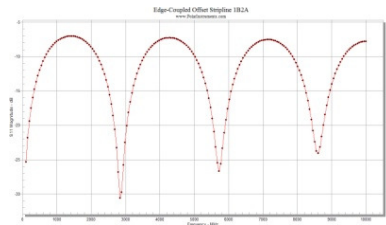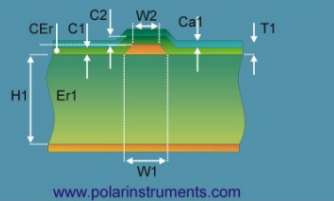Ken Taylor   (503) 356 5270

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