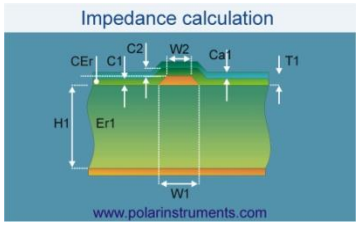
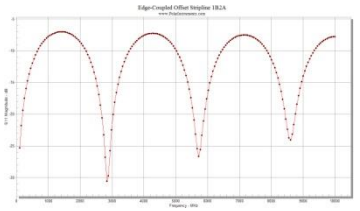
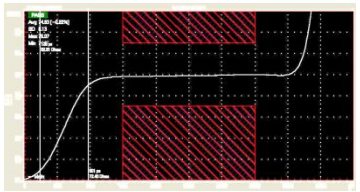
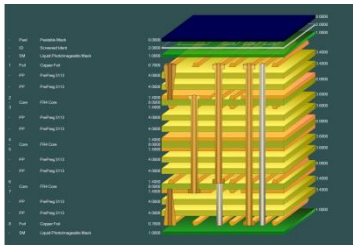
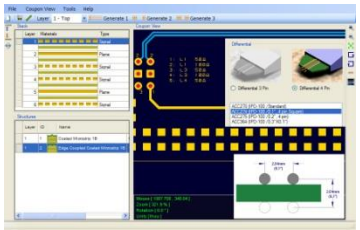










Speedstack 2017 Preview

Richard Attrill – October 2016 (Rev 3)

Introducing Speedstack 2017

Welcome to a preview of Speedstack 2017.

We have introduced a number of new features that have been requested through our Polarcare software maintenance service.

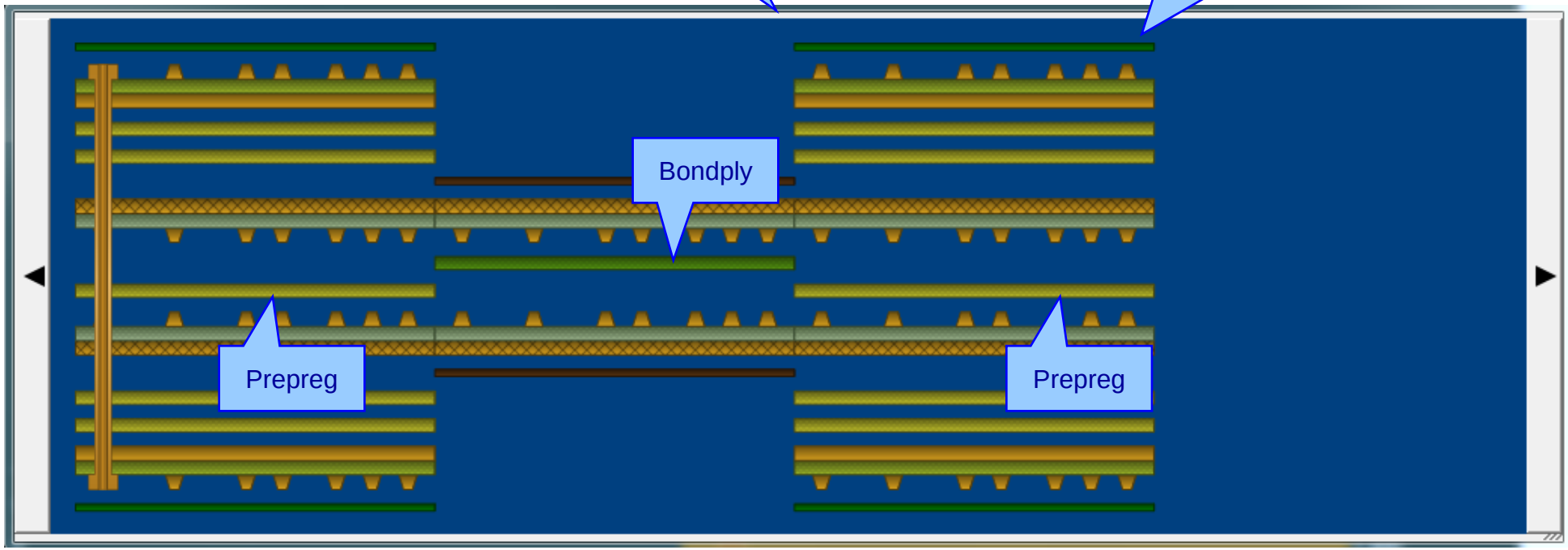
The following slides provide an overview of these new features. If you would like to have a web-based demonstration please contact your local Polar office, details are shown on the last slide of this presentation.

Speedstack Flex – Improved support for ‘bikini builds’

Example: 8 layer rigid - 4 layer flex – 8 layer rigid

Once constructed the Prepreg – Bondply – Prepreg materials are actually on the same horizontal dielectric layer – known as a ‘bikini build’

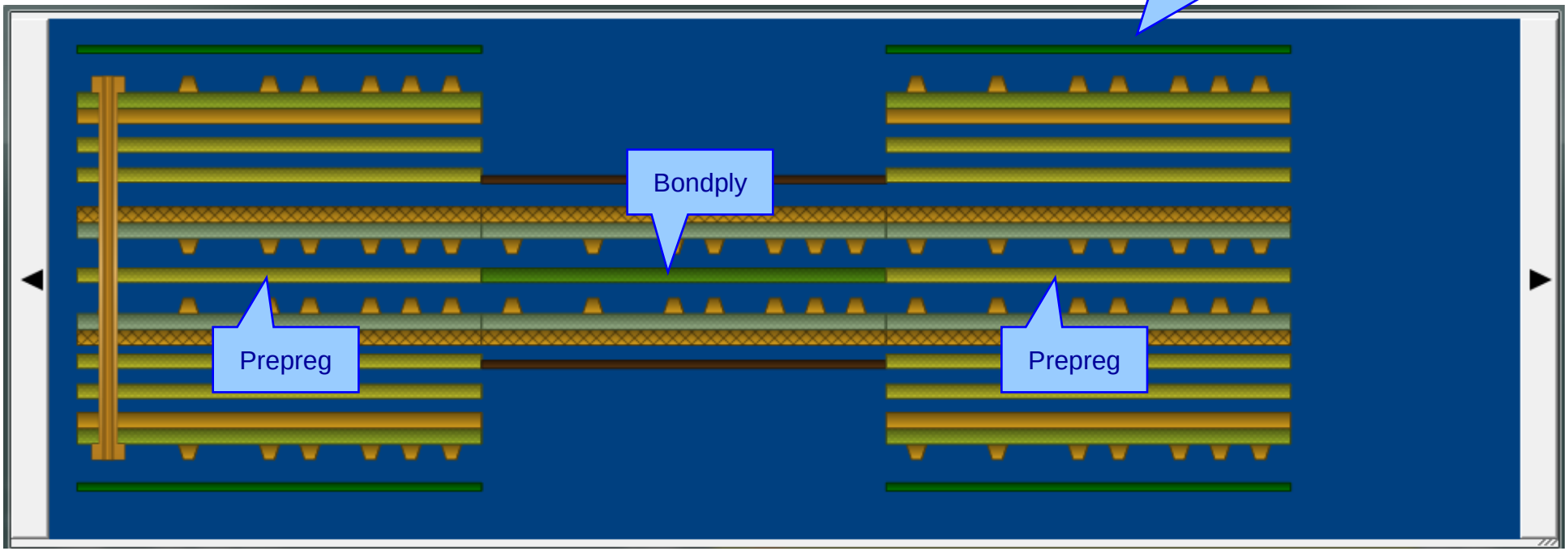
Existing Graphical Presentation



Speedstack Flex – Improved support for ‘bikini builds’

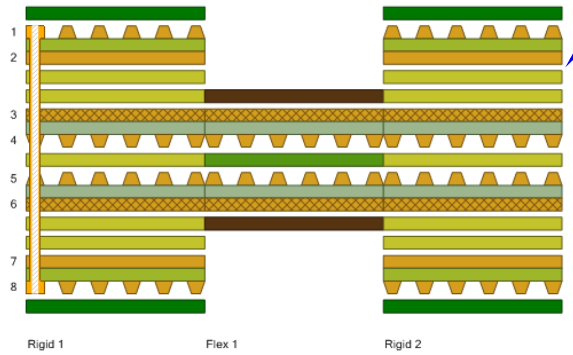
Speedstack now provides tools to allow the user to align materials in the same horizontal dielectric layer. The Prepreg and Bondply materials now align as they do in the actual rigid-flex construction

New Graphical Presentation



Speedstack Flex – Improved support for ‘bikini builds’

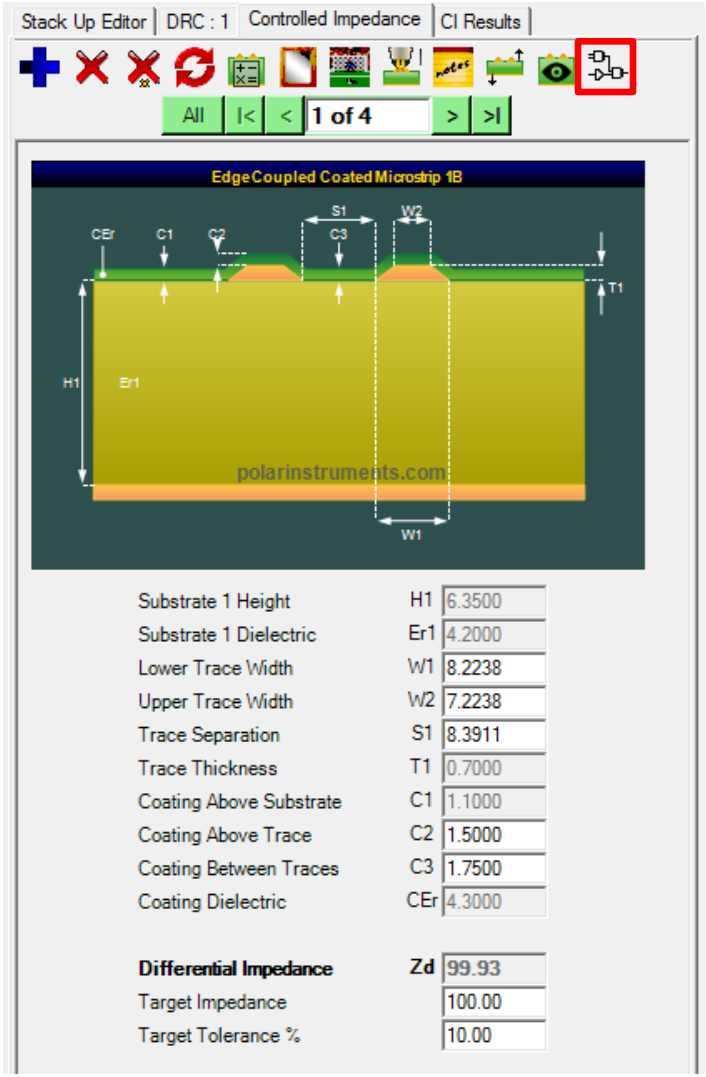
C:\Users\Richard Attrill\Desktop\PINC 24082016\3A5487D40_MB v12.stk Units: Microns

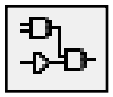


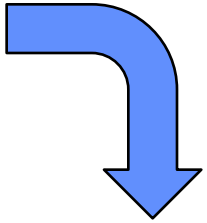
The technical report also supports different materials on the same dielectric layer, improving the clarity of documentation between the stack up designer and fabricator

StackName: Rigid 1	Version:	Revision:	Modification:	Date of Revision:	Editor		
Date:	Associated Documents:					Page 1/X	
Author:							
Department:							
Site:							

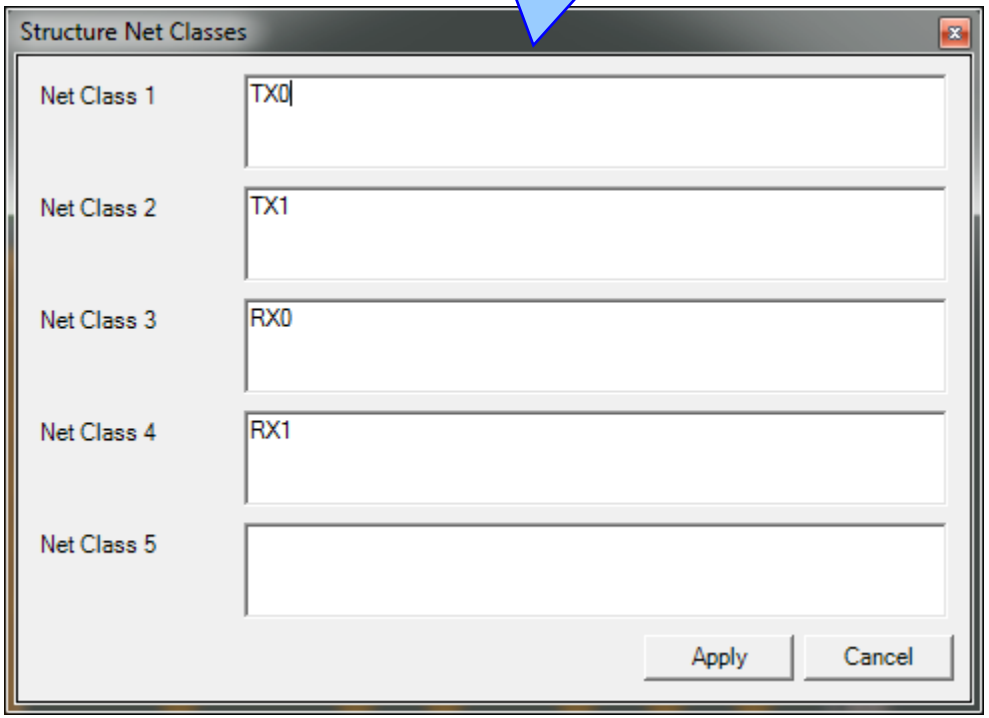
Structure Net Classes







It is now possible to store up to five Net Class names with each structure. These net class names provide a link to the matching impedance nets inside the ECAD PCB layout system.



Structure Net Classes

Selectable Net Class columns are available on the technical report

Impedance ID	Structure Image	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Target Impedance	Tol (+/- %)	Calculated Impedance	NetClass1	NetClass2	NetClass3	NetClass4	NetClass5
1		1	3	0	8.224	7.224	8.391	100.000	10.000	99.93	TX0	TX1	RX0	RX1	

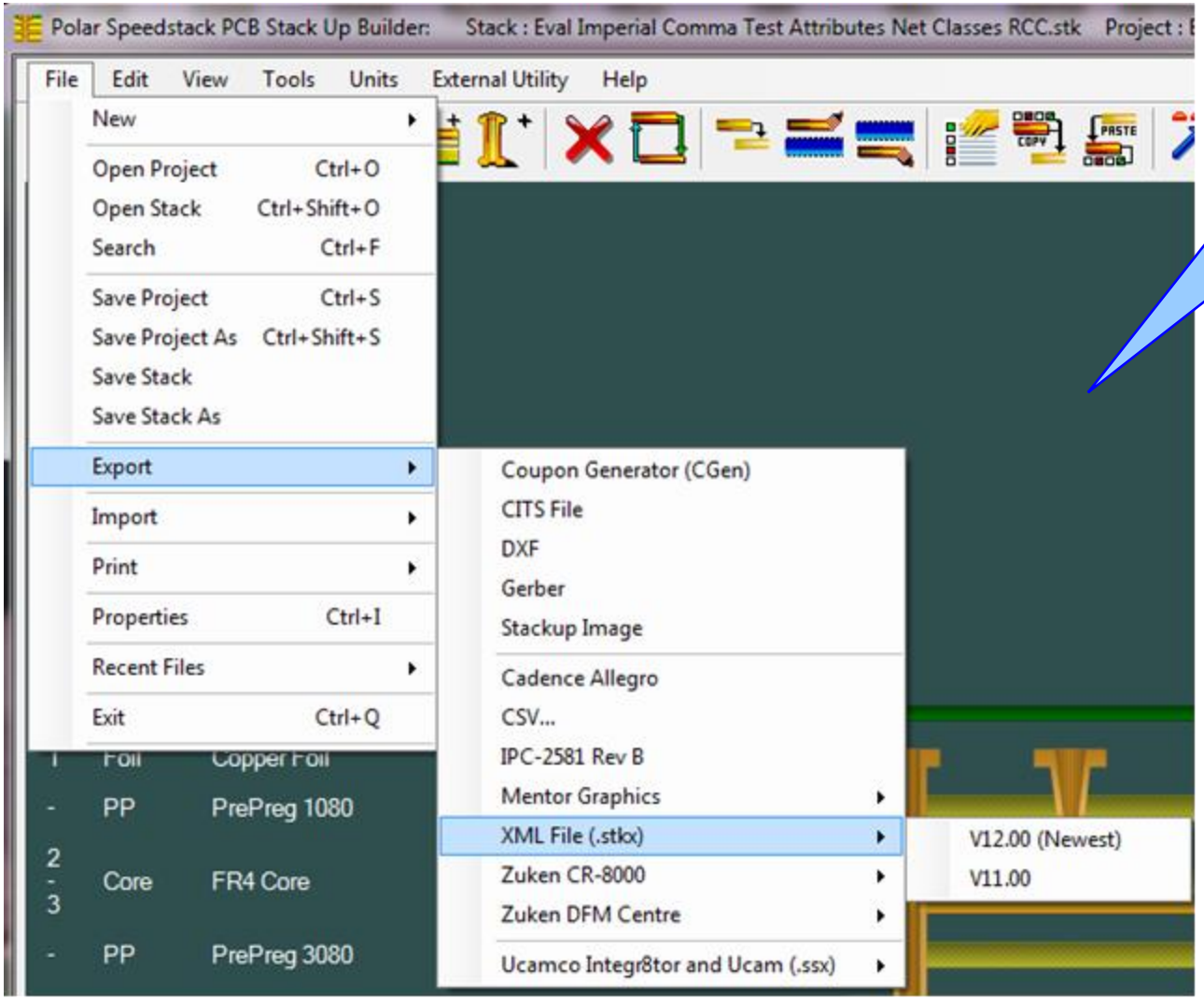
```
<NetClasses>

    <NetClass1>TX0</NetClass1>
    <NetClass2>TX1</NetClass2>
    <NetClass3>RX0</NetClass3>
    <NetClass4>RX1</NetClass4>
    <NetClass5 />

</NetClasses>
```

Speedstack now provides updated import / export XML file formats (STKX and SSX) to support Net Classes.
These file formats are commonly used to pass detailed stack up information to and from CAD / CAM systems

Extensive range of XML-based import / export options (STKX / SSX)



Speedstack is able to export and import stack up data from an ever increasing range of CAD / CAM systems.

The 2017 edition will now support STKX v11.00 and v12.00, SSX v1.00 and v2.00

Extensive range of XML-based import / export options (STKX / SSX)

A number of major enhancements have been introduced to the latest file format:

- Support for the new material attributes and structure net classes
- Support for material colours
- Improved support for structure coating profile (C1, C2, C3, CEr)
- More comprehensive structure parameter information including these extra parameters - <StructureNumber>, <H1>, <Er1>, <H2>, <Er2>, <H3>, <Er3>, <H4>, <Er4>, <C1>, <C2>, <C3>, <CEr>, <NetClass1>, <NetClass2>, <NetClass3>, <NetClass4>, <NetClass5>
- Simplified versioning information

Technical report enhancements

Impedance ID	Structure Image	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Target Impedance	Tol (+/- %)	Calculated Impedance	Trace Pitch (S1+ W1)	Coating Above Substrate (C1)	Coating Above Trace (C2)	Coating Between Traces (C3)	Coating Delectric (CEr)
1		1	3	0	8.224	7.224	8.391	100.000	10.000	99.930	16.615	1.100	1.500	1.750	4.300
2		1	3	0	4.500	3.500	0.000	75.000	10.000	75.270	0.000	1.100	1.000	0.000	4.300

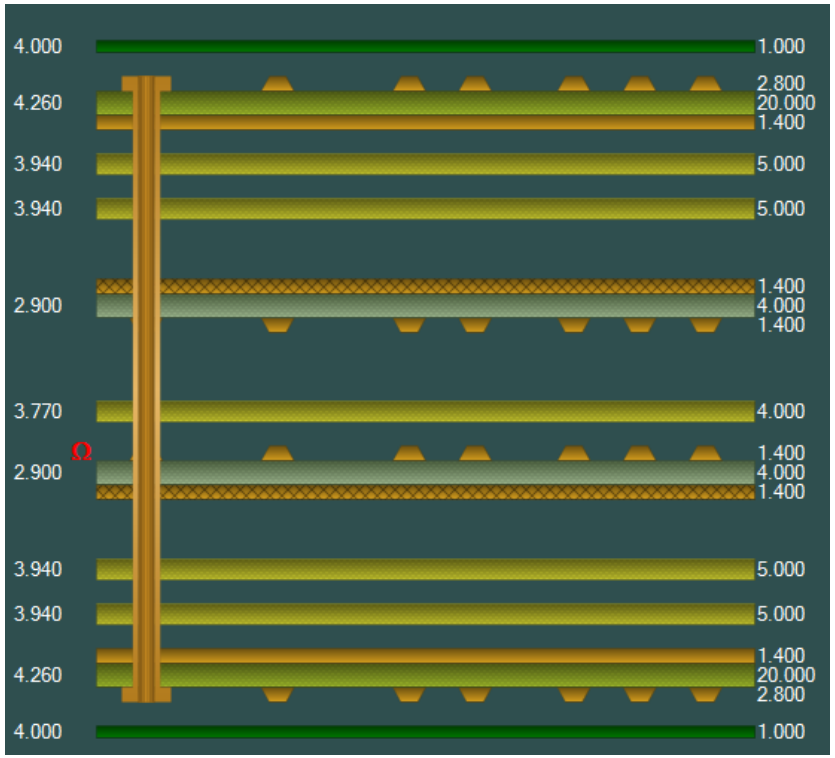
New selectable columns have been introduced to the impedance structure table:

- Trace Pitch
- More detailed coating profile information, separate columns for each coating parameter

Layer	Stack up	Supplier	Supplier Description	Description	Type	Processed Thickness
	STSL					
		Polar Samples	SM/001	Liquid PhotoImageable Mask	SolderMask	1.100
1		Polar Samples	FO/001	Copper Foil	Copper	0.700
		Polar Samples	PP/001	PrePreg 1080	Dielectric	1.950
2						1.400
		Polar Samples	CO/005	FR4 Core	FR4	3.000
3						1.400

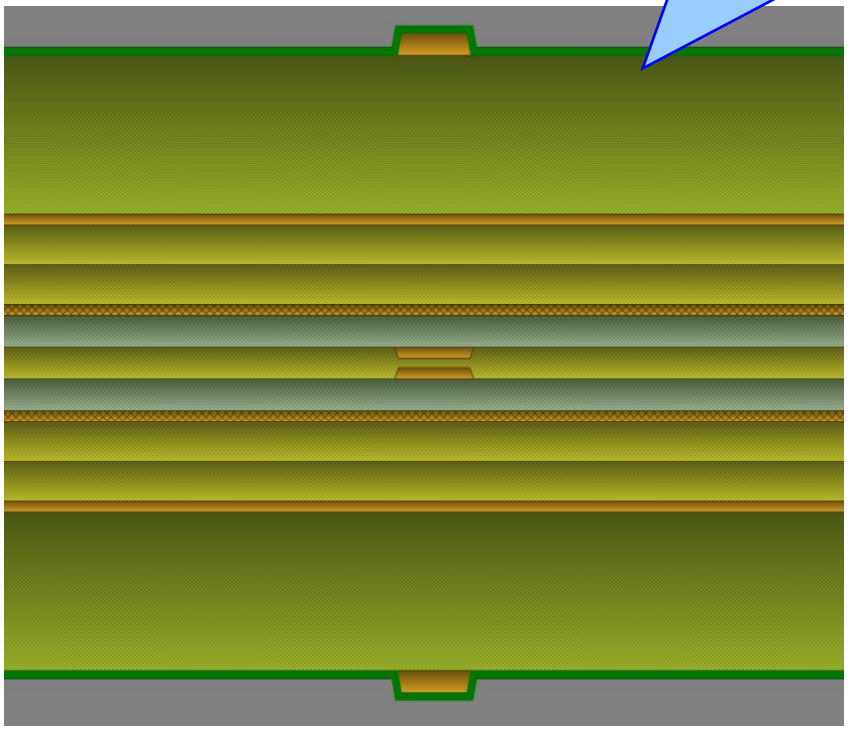
The Processed Thickness column now includes the Solder Mask thickness

BETA Experimental Feature : Proportional View



Your input is valuable to us!!

Do you feel there is value to having a proportional view of the stack up?

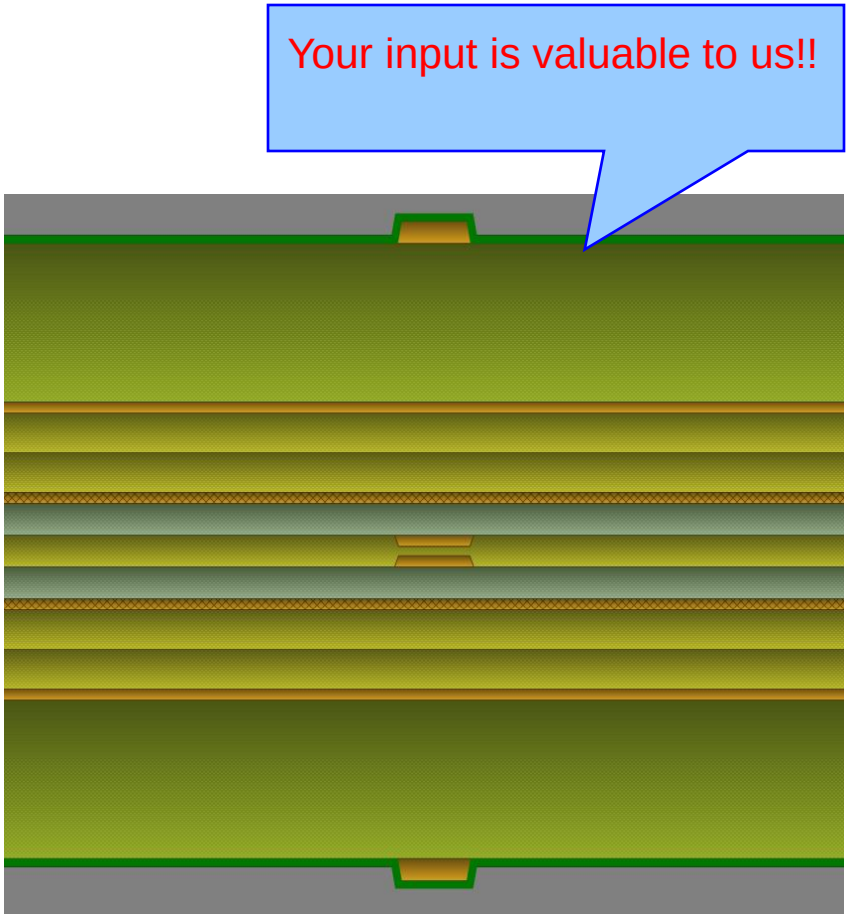


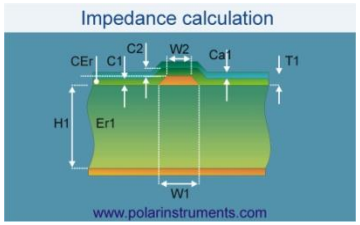
Some users have expressed interest in displaying the stack up where the material thicknesses are proportional to each other. This can be informative as a visual aid, especially when considering the dielectric thicknesses between electrical layers

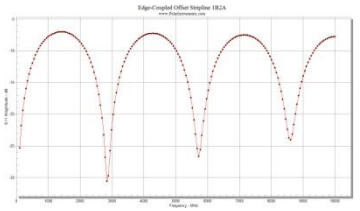
BETA Experimental Feature : Proportional View

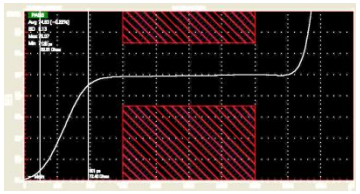
If you feel this would be a valuable addition to Speedstack please could you send your comments to:
polarcare@polarinstruments.com

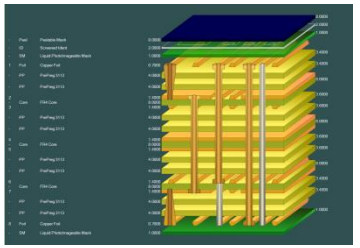
We would be appreciative of any feedback you have at this early stage of development.

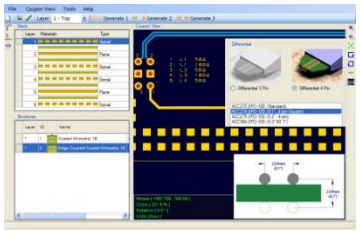




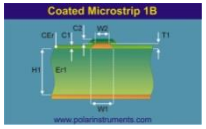


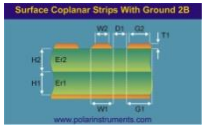


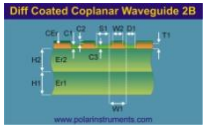


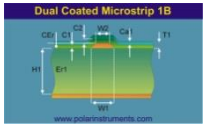


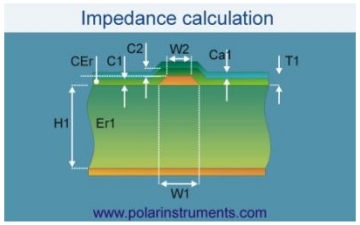
Thank you

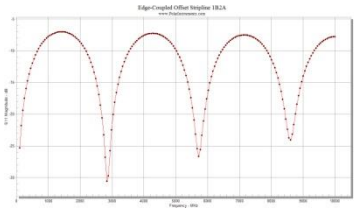


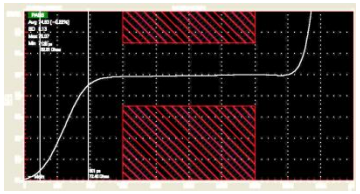


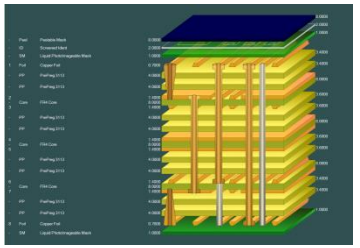


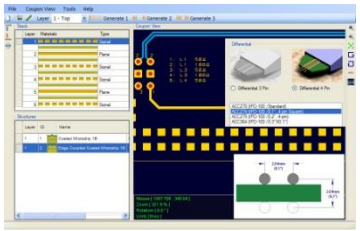












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