



Coated Microstrip 1B Edge-Coupled Offset Stripline 1B1ATR Surface Coplanar Strips 2B Diff Coated Coplanar Waveguide 2B Dual Coated Microstrip 1B Edge-Coupled Offset Stripline 2B1AT

Speedstack 2020 Release

Richard Attrill – January 2020 (Rev 2)



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Introducing Speedstack 2020

Welcome to Speedstack 2020.

We have introduced a number of new features that have been requested through our Polarcare software maintenance service.

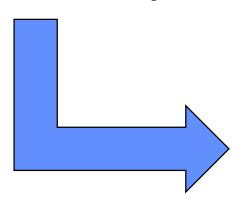
If you would like to have a web-based demonstration please contact your local Polar office, details are shown on the last slide of this presentation.



Drilling enhancements

🕌 Add Drill		×
Column	First Electrical Layer No	Second Electrical Layer No 8
Drill Information C Mechanical Image: Laser C Laser (Stacked) Image: Through Plated Data Filenames	Fill Type No Fill	Hole Information Hole Count 0 Different Hole Sizes 0 Minimum Hole Size 0.0000

Current Add Drill dialog



Electrical Layers Stack Up Column 3	First Electrical Layer Second I No 1 2	Electrical Back Drill Mu Layer No	ust Cut Back Drill Must Not Cut Layer No
Drill Information C Mechanical C Laser C Laser (Stacked) C Back Drill ▼ Through Plated Data Filenames	Fill Type Resin	Hole Information Hole Count 1000 Different Hole Sizes 2 Minimum Hole Size 20.0000 Minimum Pad Size 25.0000	Minimum Drill Size 25.0000 Minimum Drill Size Tolerance (Abs) 5.0000 Minimum Barrel Wall Thickness 3.0000
Back Drill Information Minimum Distance Fro 0.0000 Maximum Distance Fr 0.0000 Primary Drill Size 0.0000	om Cut Layer		Now supports: 1. New Back Drill type 2. 5 x Notes fields 3. Additional parameter 4. Graphical preview

Enhanced Add Drill dialog with improved functionality

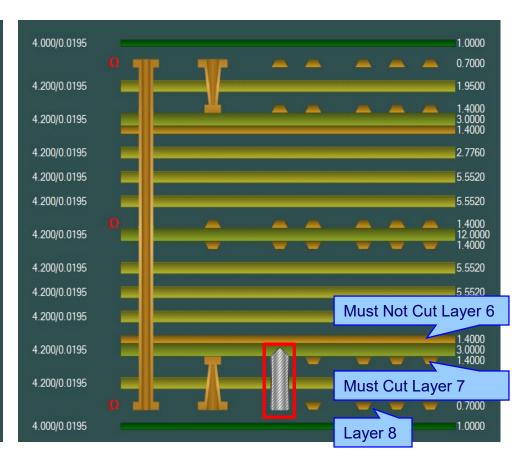


Speedstack 2020 Introduction

New Back	<u>k Drill type</u>	Back drills require the nomination of the Must Cut
	Add Drill	Layer (MC) and Must Not Cut Layer (MNC)
	Main Notes Electrical Layers First Electrical Layer Second Electrical Layer No Back Drill Must Cut Back Drill Must Not Cut Layer No 3 • 8 • 7 • 6 •	
New Back Drill option	Drill Information Hole Information O Mechanical Fill Type O Laser No Fill	
option	C Laser (Stacked) Different Hole Sizes Minimum Drill Size Tolerance Back Drill 5.0000 5.0000 Through Plated Minimum Hole Size Minimum Barrel Wall Thickne Data Filenames 0.0000 0.0000 Minimum Pad Size 0.0000	
Important Back Drill information is entered here	Back Drill Information Minimum Distance From Cut Layer 5.0000 Maximum Distance From Cut Layer 5.0000 Primary Drill Size 30.0000	ded, in
	Add	Close





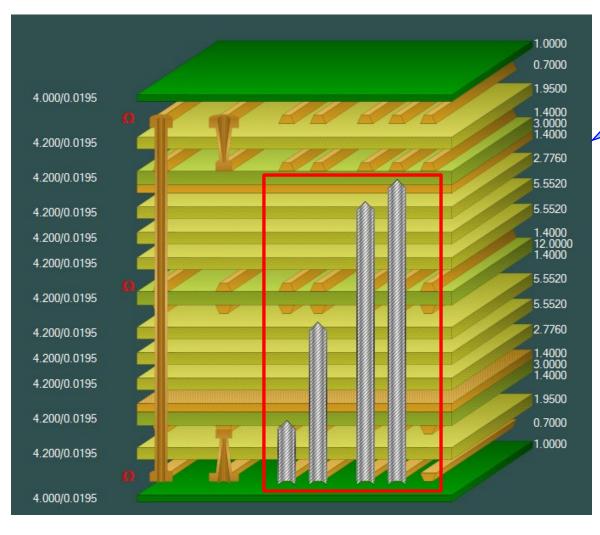


Back Drill added to the stack up. 3D and 2D images shown

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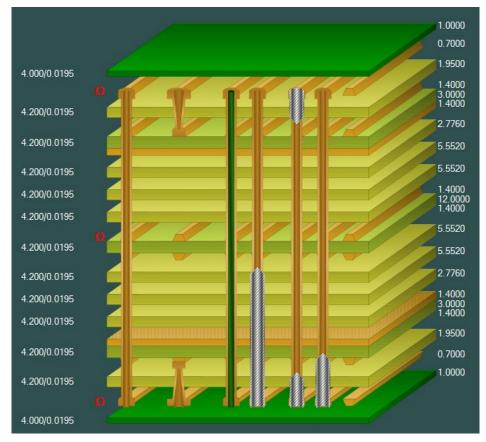


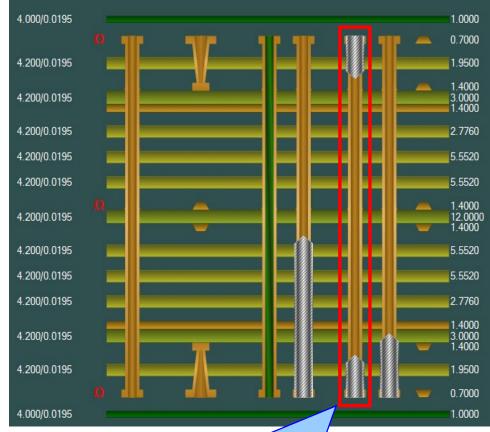
Other examples of the new Speedstack Back Drill capability. Although this is not a 'real world' stack up it does show how back drills can be added using the Must Cut / Must Not Cut methodology



C:\Program F	iles\Polar\Sp	eedstack\Samples\E	val Imperial E	Back Drill v	v17.sci	ι	Jnits: Mils										_	P ₂		I						
Layer	_	Stack up			Description	TI	rocessed hickness		Loss Tangent		ance ID]						
			_	Liquid Ph	notolmageable		1.000	4.000	0.0195																	
1					Coppe		0.700				1, 2															
					PrePreg	1080	1.950	4.200	0.0195										The	Char	- det	ade	to oby	sin al		
2					FR4	Core	1.400	4.200	0.0195											Spee						
3			~ 0		DeeDees	2000	1.400	4 000	0.0405										repo	ort op	tion	has	beer	า		
					PrePreg		2.776		0.0195																	
					PrePreg PrePreg		5.552 5.552		0.0195										enh	anced	d to	docu	umer	nt the	ne	N
4 8					PrePreg	1651	1.400	4.200	0.0195		2															
4 00 5 7600 5					FR4	Core	12.000 1.400	4.200	0.0195		3								вас	k Dril	ı ca	pabli	ity.			
а 6					PrePreg	1651	5.552	4 200	0.0195																	
					PrePreg		5.552		0.0195									•								
					PrePreg			4.200	0.0195																	
6					FIGFIC	10000	1.400	4.200	0.0155																	
-					FR4	Core	3.000	4.200	0.0195																	
7							1.400																			
					PrePreg		1.950	4.200	0.0195																	
8 🔻			/// V//		Coppe		0.700				4															
					notoImageable		1.000		0.0195									ldermask = 61.460		-						
Impedance ID	Structure Image	Impedance Ref. Signal Plane Layer in Laye	Ref. 1 Plane 2	Lower Trace	Upper Trace Tr Width Sepa	race	Target npedance	Tol (+ %)		1		•			•											
1		1	3 0	8.500	7.500	8.115	100.000	10.00	0 100.350)										1						
2		1	3 0	4.500	3.500	0.000	75.000	10.00	0 75.870)																
3		4	3 6	7.250	6.250	8.500	100.000	10.00	0 101.28)																
4		8	6 0	4.500	3.500	0.000	75.000	10.00	0 75.870)																
StackName: I	Master																									
Drawing No:			Associated	Documen	its:				Re	vision: M	odification:		Date of Revisi	ion: E	ditor					1						
Date:																										
Author:																Page 1/X	"									
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It is also possible to overlay Back Drills on top of existing through-plated drills, to document the complete back drilled via structure



C:\Program	n Files\Polar\Speedstack\Samples\Eval Imperial	Back Drill Keep Vias v17.sci	Un	its: Mils				
Layer	Stack up	Description	Processed Thickness	er l	Loss Tangent	Impedance ID]
		Liquid PhotoImageable Mask	1.000	4.000	0.0195			
1		Copper Foil	0.700			1,2		
		PrePreg 1080	1.950	4.200	0.0195			
2 3		FR4 Core	1.400 3.000 1.400	4.200	0.0195		The	e technical report showing
		PrePreg 3080	2.776		0.0195		the	complete back drilled via
		PrePreg 1651	5.552		0.0195			-
		PrePreg 1651	5.552 1.400	4.200	0.0195		stru	cture
4 097 5 09		FR4 Core	12.000	4.200	0.0195	3		
5 2		PrePreg 1651	1.400 5.552	4 000	0.0195			
		PrePreg 1651 PrePreg 1651	5.552		0.0195			
		PrePreg 3080	2.776		0.0195			
6			1.400					
7		FR4 Core	3.000 1.400	4.200	0.0195			
		PrePreg 1080	1.950	4.200	0.0195			
8		Copper Foil	0.700			4		
		Liquid PhotoImageable Mask	1.000		0.0195			_
		Copper Thickness = 9.800 Di	electric Thickne	ess = 49.6	560 Solder Ma	ask Thickness = 2.000 Stack Up Thickness = 59.460 Stack Up Thicknes	ss with Soldermask = 61.460	
		Lower Upper						1
Impedanc	ce Structure Signal Plane 1 Plane 2	Trace Trace Trace Width Width Separation	Target	Tol (+/-	Calculated			
ID	Image Layer in Layer in Layer	(W1) (W2) (S1)	Impedance	%)	Impedance			
	1 3 0	8.500 7.500 8.115	5 100.000	10.000	100.350			_
	2 1 3 0	4.500 3.500 0.000	75.000	10.000	75.870			
	3 4 3 6	7.250 6.250 8.500	0 100.000	10.000	101.280			•
	4 8 6 0	4.500 3.500 0.000	75.000	10.000	75.870			

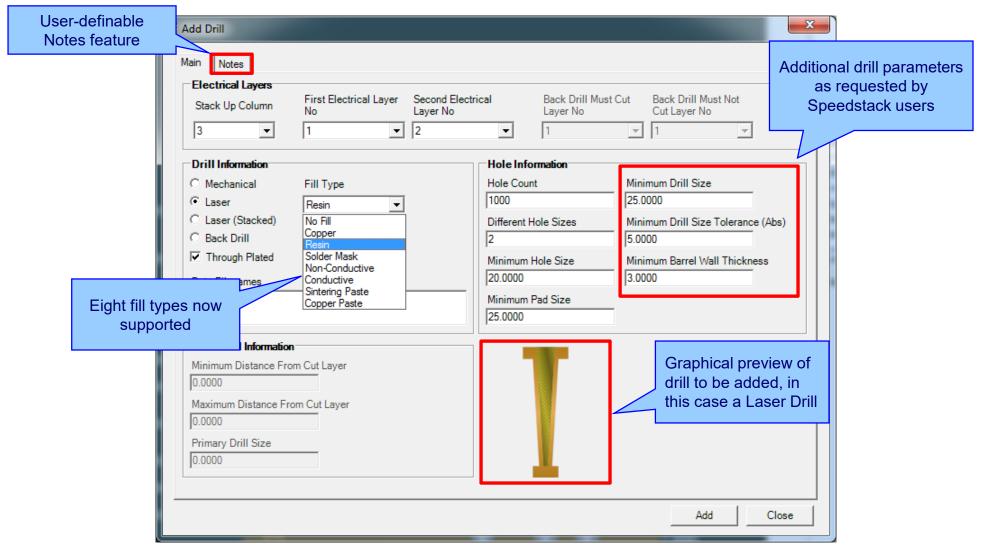
				_				
StackName: Master								
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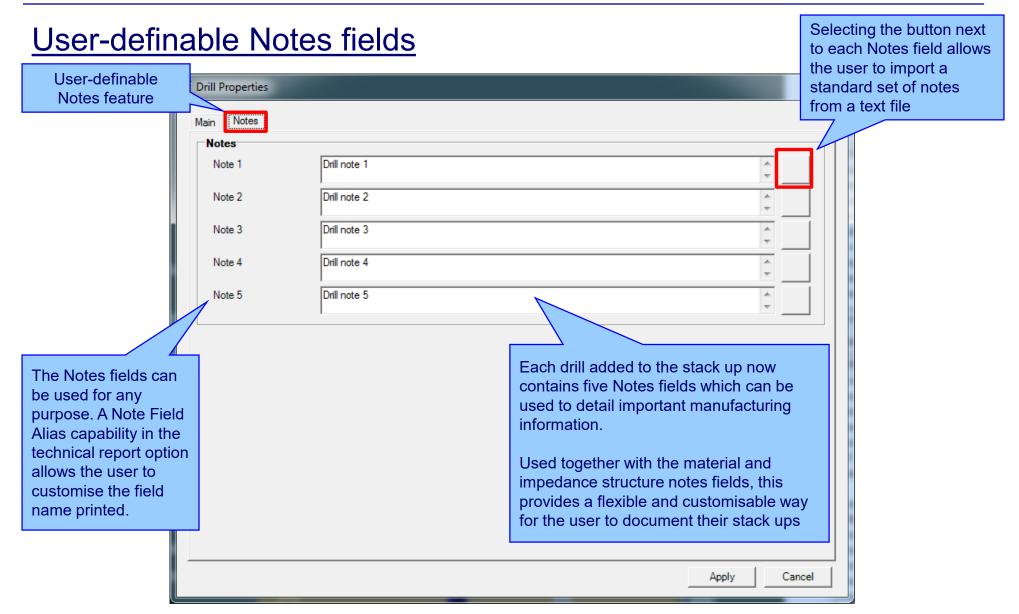
9



Other Drill enhancements









Copper Finishing enhancements 🗱

Percentage Copper Set by Layer type Signal La Mixed La Plane Lay	yer % 7 yer % 1	15	 The Apply Finishing option now supports multiple user-nominat copper finishing values. Depending upon the PCB Class selected, a different amount of copper plating is applied to the
C Proportional to C Copper Finishing Enter values of thick		e. The selected value will be	base copper
Name Class 1	Value	Selection	
Class 2 Class 3 Class 4	1.0000 1.4000 2.8000		



Stack Up Wizard – now supports build up to 128 layers

Canala Lin Winned (Matanial Library M	The Stack Up Wizard has been enhanced to support up to 128		
Foil Copper Foil FO/001	layers Iayers Iask SM/ Clear	Planes and Mixed Layers Image: Symmetrical Plane Layers 1 2 3 4 5 6 7 8	Mixed Layers
Prepreg PrePreg 1080 PP/001 Prepreg PrePreg 1080 PP/001	Clear	Clear	Clear
Prepreg Core FR4 Core CO/008	Clear	Drilling Through-Plated	Non Through-Plated
Stack Up Thickness: 813.7996 (Mils)		Stack Up Thickness with Soldermas	Apply Cancel k: 815.7996 (Mils)



Stack Up Wizard – now supports build up to 128 layers

	1	ck Stack Up Builder: Stack : Untitled.stk P		100-00	inge term		-						
ile j	dit <u>V</u> i	iew Iools Units External Utility Hel		· · · · · · · · · · · · · · · · · · ·		_ ~		4 7	- Jack	1° _ "	<u>~</u>		
	_ 4					~ ~		T 1		🦉 🥭 🗖			
	P	PrePreg 1080	4.200/0.0195							3.0000	S	tack Up Editor DRC : 1 Controlled Imp	edance CI Results
	P	PrePreg 1080	4.200/0.0195				_		_	3.0000			
16 17	ore	FR4 Core	4.200/0.0195				=			1.4000 4.0000 1.4000			
	P	PrePreg 1080	4.200/0.0195							3.0000	ſ	Stack Up Information	
												Field	Value
	Р	PrePreg 1080	4.200/0.0195							3.0000		Electrical Layer Count Stack Up Cost	128 635.00
8	ore	FR4 Core	4.200/0.0195		-	-	-	-	- 4	1.4000 4.0000		Copper Thickness	177.8000
19					-	-	-	-		1.4000		Dielectric Thickness Solder Mask Thickness	636.0000
	Р	PrePreg 1080	4.200/0.0195			_	_	_	_	3.0000			
	P	PrePreg 1080	4.200/0.0195							3.0000		Target Stack Up Thickness	60.0000 813.8000
20					1	-	-	-	-			Stack Up Thickness Stack Up Thickness with Soldermask	815.8000
20 21	ore	FR4 Core	4.200/0.0195		-		<u> </u>	-		1.4000 4.0000 1.4000		*****	
21												Selected Item Information	
	P	PrePreg 1080	4.200/0.0195					_		3.0000			
	P	PrePreg 1080	4.200/0.0195			_	_		_	3.0000			
22										1.4000			
23	ore	FR4 Core	4.200/0.0195							4.0000			
	ø	PrePreg 1080	4.200/0.0195							3.0000			
	P	DepDeed 1090	4 200/0 0105						_	3.0000			
24						-	-	-	- 4				
25	° In	n this example the la	ast				_			4.0000 1.4000			
		lectrical layer of the								3.0000			
		-	SIGUN							3.0000			
	u	p is 128					-		-				
126	ic.				-		<u> </u>	-	A 4	1.4000 4.0000 1.4000			
127										1.4000			
	P	3 TU80	4.200/0.0195				_			3.0000			
	P	PrePreg 1080	4.200/0.0195							3.0000			
128	oil	Copper Foil			-	-	-	-	-	0.7000			
	M	Liquid Photolmageable Mask	4.000/0.0195							1.0000			
			1.000/0.0100							1.0000			
		Mis/Thous	Target Stack Up Thickn			2.0000	Designed	Thisland		sk = 815.8000 Beta V : 1	9 10 17722		

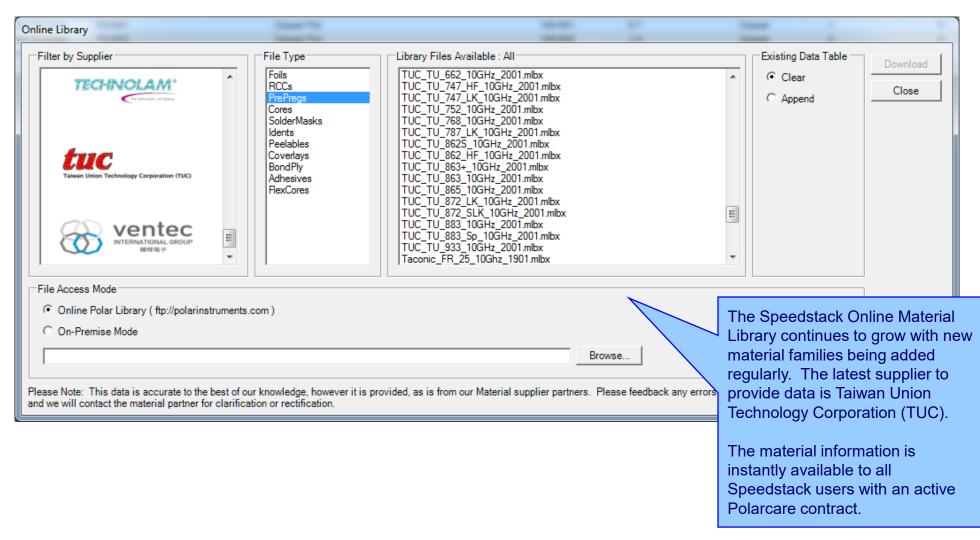


Stack Up Wizard – now supports build up to 128 layers

	Stack Up Information	
	Field	Value
	Electrical Layer Count	128
	Stack Up Cost	635.00
	Copper Thickness	177.8000
	Dielectric Thickness	636.0000
Zoom extents shows the	Solder Mask Thickness	2.0000
whole 128 layers. The	Target Stack Up Thickness	60.0000
	Stack Up Thickness	813.8000
mouse-wheel zoom	Stack Up Thickness with Soldermask	815.8000
capability allows for easy	h	
	Selected Item Information : Core	
editing of specific areas of	Field	Value
	Supplier	Polar Samples
the build	Supplier Description	CO/008
	Description	FR4 Core
	Stock Number	400-008
	Type	FR4
		1.151
	Upper Cu Base Thickness	1,4000
	Upper Cu Finished Thickness	1.4000
	Upper Copper Coverage	0
	Minimum Trace Width	2.9528
	Data Filenames	
	Dielectric Base Thickness	4.0000
	Dielectric Finished Thickness	4.0000
	Dielectric Constant	4.2
	Loss Tangent	0.0195
	Resin Content	53
	Tg	180
	Td	0
	CAF Resistance	0
	Z Axis Expansion	0
	Excess Resin	0.0000
	Isolation Distance	4.0000
	Isolation Distance	4.0000
	Lawren Co. Prove Thirdrener	1 4000
	Lower Cu Base Thickness Lower Cu Finished Thickness	1.4000



Online Material Library



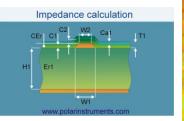


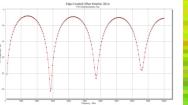
Import / Export enhancements

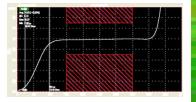
The following Import / Export options have been updated to support new fields introduced with Speedstack 2020:

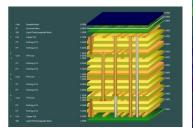
- XML STKX v17.00 and SSX v7.00 import / export options
- CSV export option











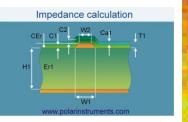


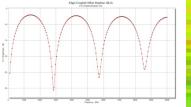
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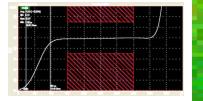


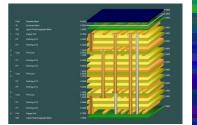
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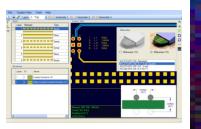


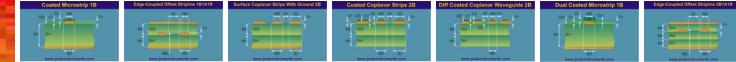












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