













Speedstack 2021 Preview

Richard Attrill – July 2021 (Rev 4)





Introducing Speedstack 2021

Welcome to a preview of Speedstack 2021.

We have introduced a number of new features that have been requested through our Polarcare software maintenance service.

If you would like to have a web-based demonstration please contact your local Polar office, details are shown on the last slide of this presentation.

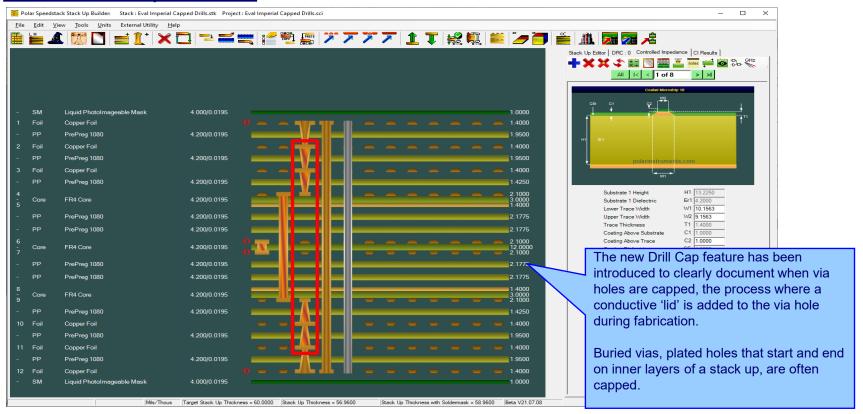
Please note: the Speedstack units have been set to Mils in the following screen grabs



Speedstack v21.07.08 (July 2021)

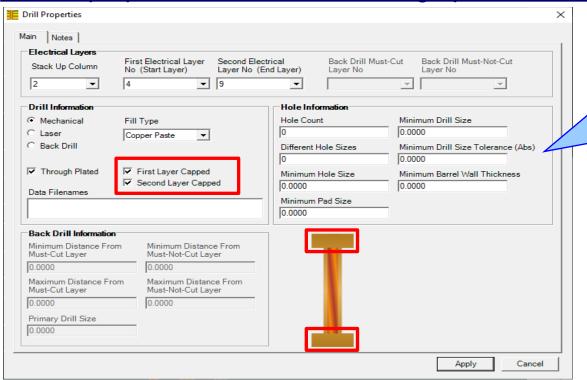


New Drill Cap feature





Drill Cap option – mechanical through plated drills



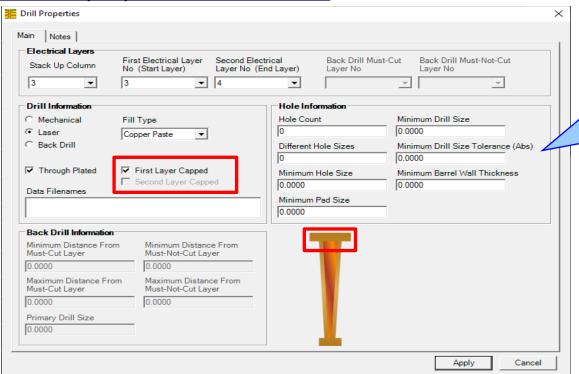
Mechanical

For mechanical drills it is possible to have four states:

- 1. Neither first or second layer capped (default when adding a drill)
- 2. First layer capped
- 3. Second layer capped
- 4. Both layers capped



<u>Drill Cap option – laser drills</u>



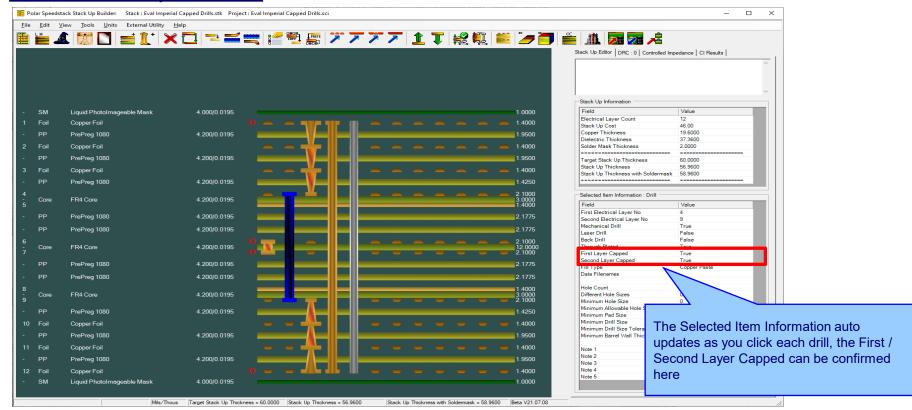
Laser

For laser drills it is possible to have two states as the Second Layer Capped checkbox is disabled:

- Not capped (default when adding a drill)
- 2. First layer capped

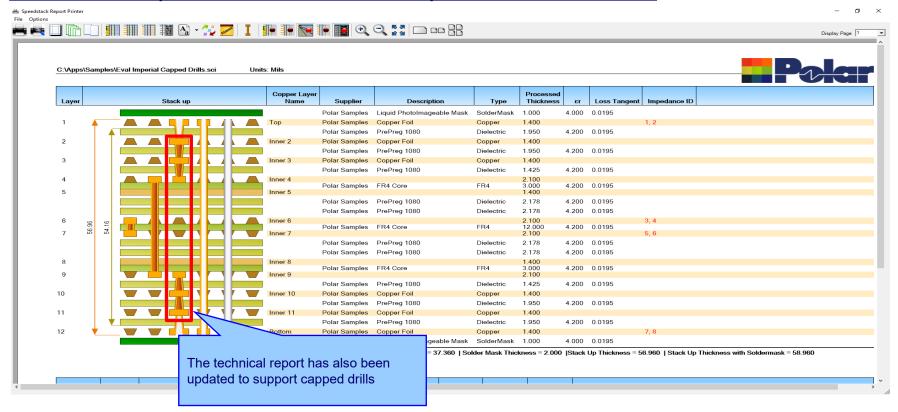


New Drill Cap feature





New Drill Cap feature - technical report enhancements





Import / Export enhancements

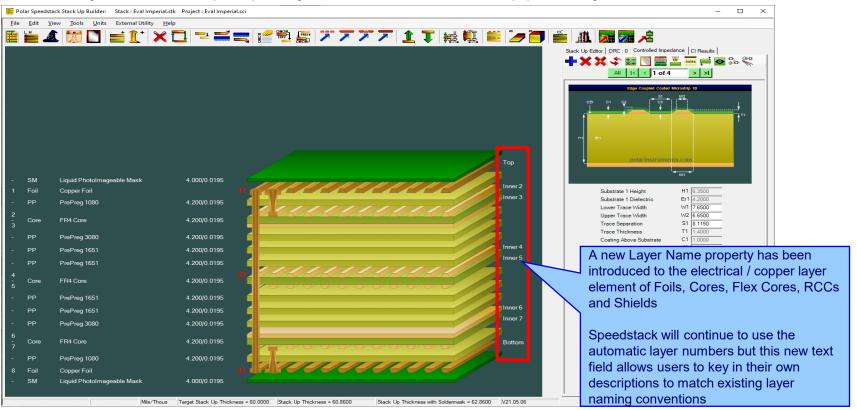
The following Import / Export options have been updated to support the drill cap properties introduced with Speedstack v21.07.08:

- XML STKX v22.00 and SSX v12.00 import / export options
- CSV export option

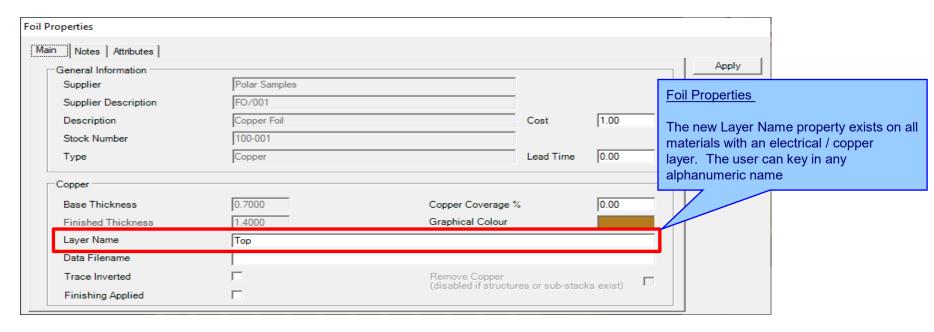


Speedstack v21.05.06 (May 2021)

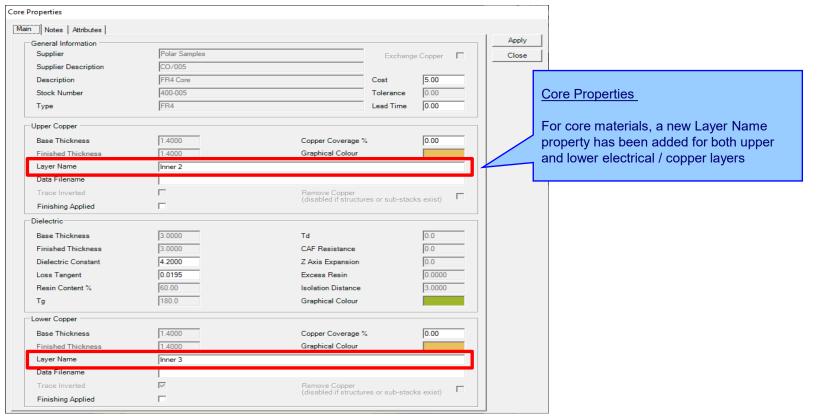




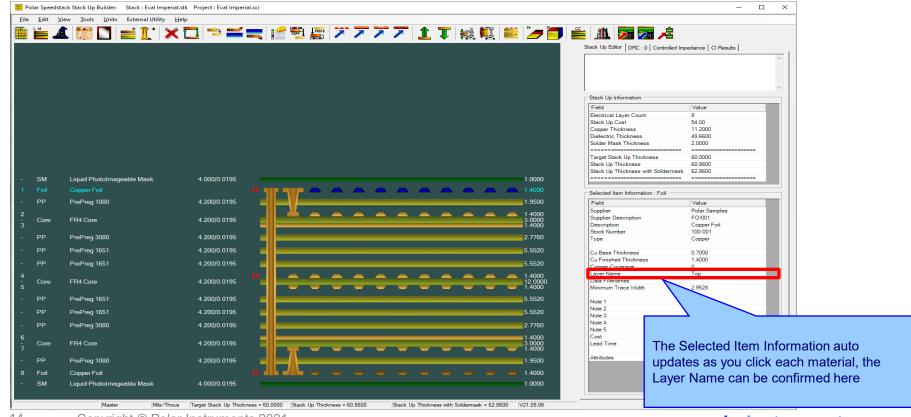




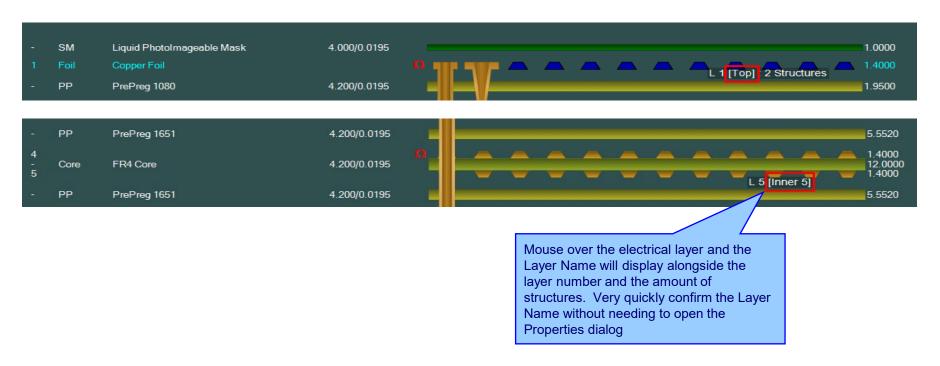




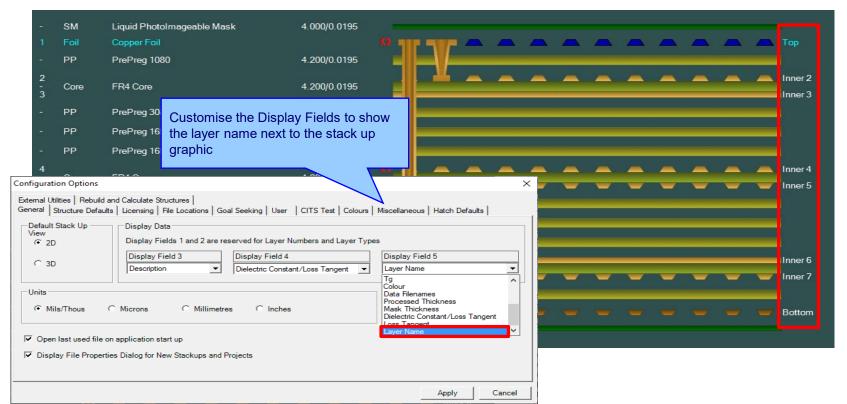




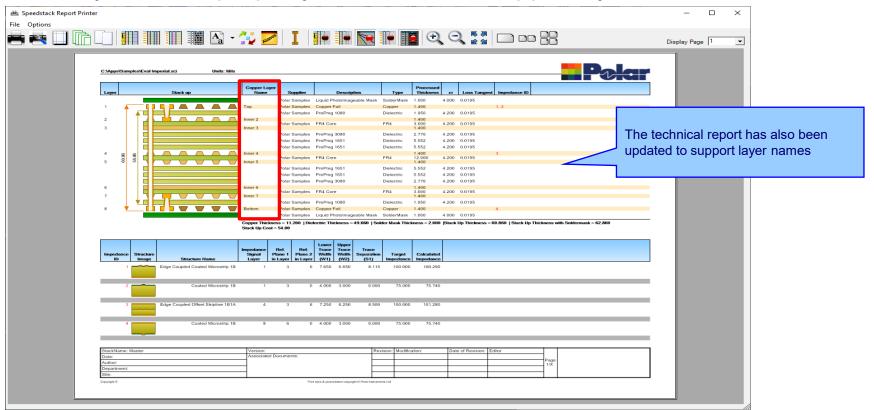






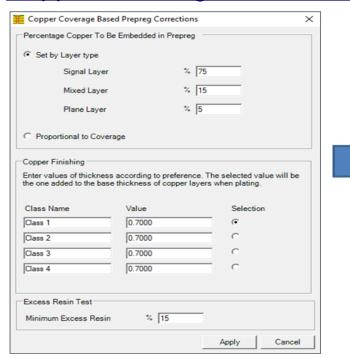




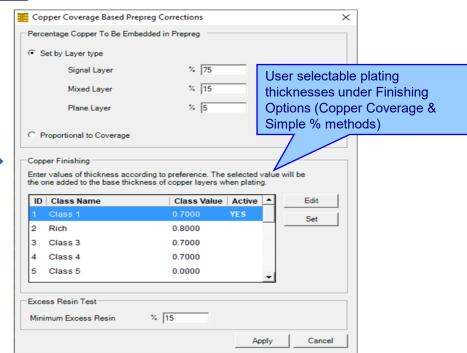




Copper Finishing classes increased



Speedstack v21.04 and earlier supported 4 classes



Speedstack v21.05 now supports 20 classes



Import / Export enhancements

The following Import / Export options have been updated to support the layer name property introduced with Speedstack v21.05.06:

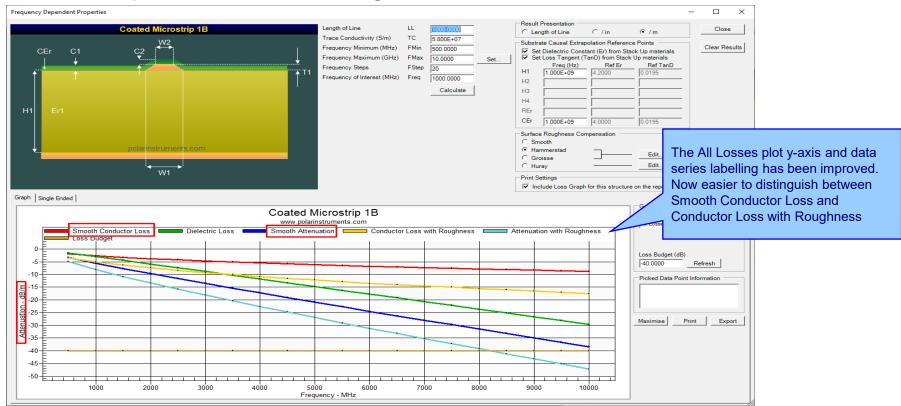
- XML STKX v21.00 and SSX v11.00 import / export options
- CSV export option
- Gerber / DXF export option



Speedstack v21.04.00 (April 2021)



All Losses plot - clearer labelling





Other enhancements

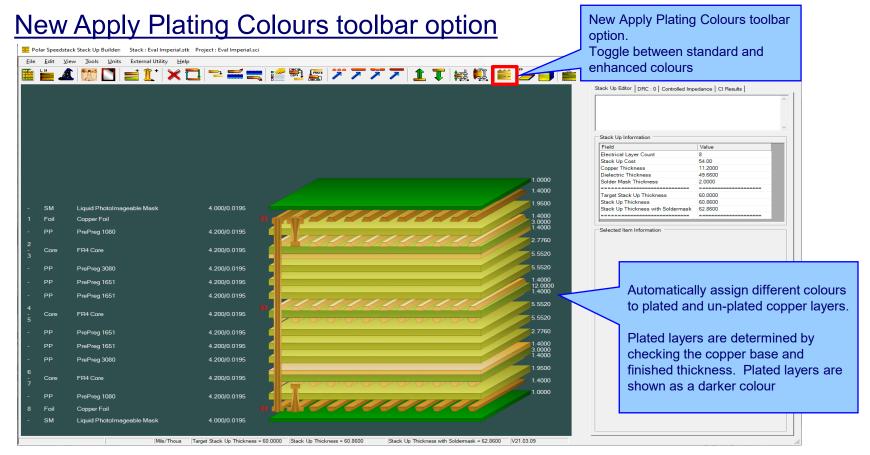
- The controlled impedance and insertion loss Calculation Engine updated to the latest edition
- Frequency Dependent Calculations graphing library enhancements



Speedstack v21.03.09 (March 2021)

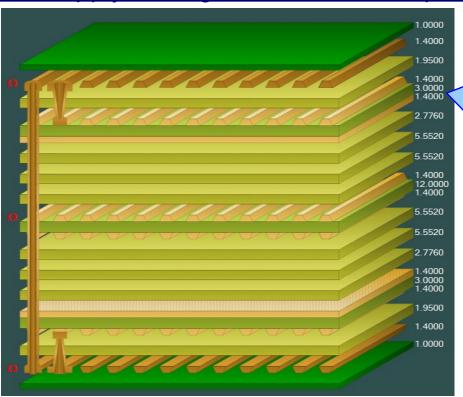








New Apply Plating Colours toolbar option



Plated Copper Layers

During PCB fabrication drill holes commonly have copper applied to the barrel wall by an electroplating process. This provides an interconnect between copper layers in the stack up.

This electroplating process often results in additional copper also being applied to the exposed copper layers where the mechanical drill starts / ends.

It is important to account for this additional plated copper thickness when calculating the overall stack up thickness and controlled impedance / insertion loss structures

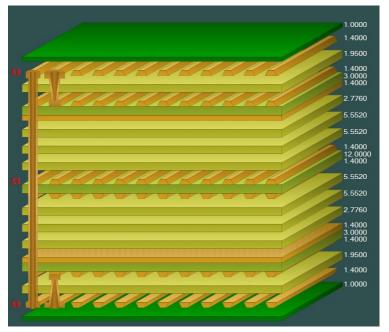
Speedstack has always allowed this additional plating thickness to be applied to the relevant copper layers. With v21.03 this has been enhanced further with automatic colour assignments to the plated and un-plated layers.



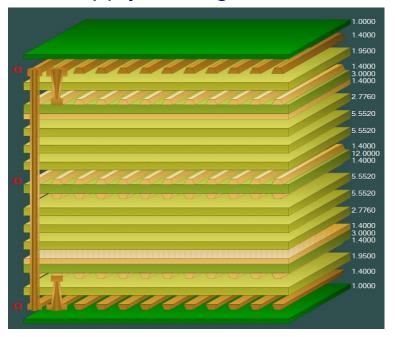
26

New Apply Plating Colours toolbar option



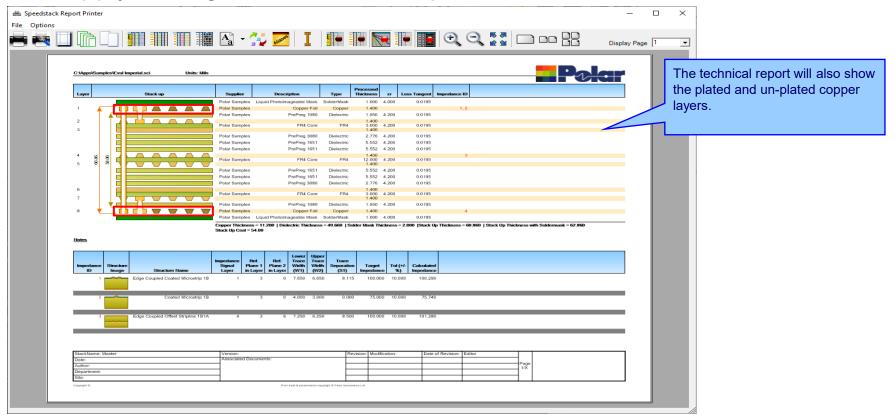








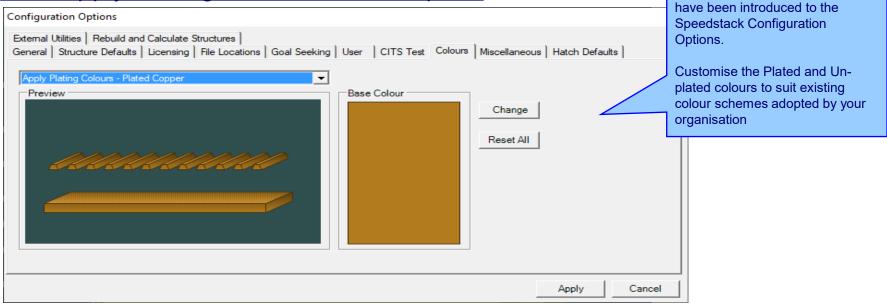
New Apply Plating Colours toolbar option



Two new user-definable colours

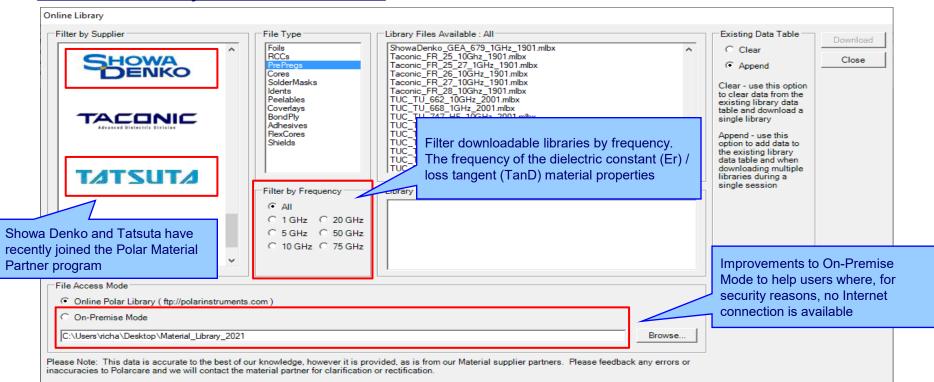


New Apply Plating Colours toolbar option





Online Library enhancements



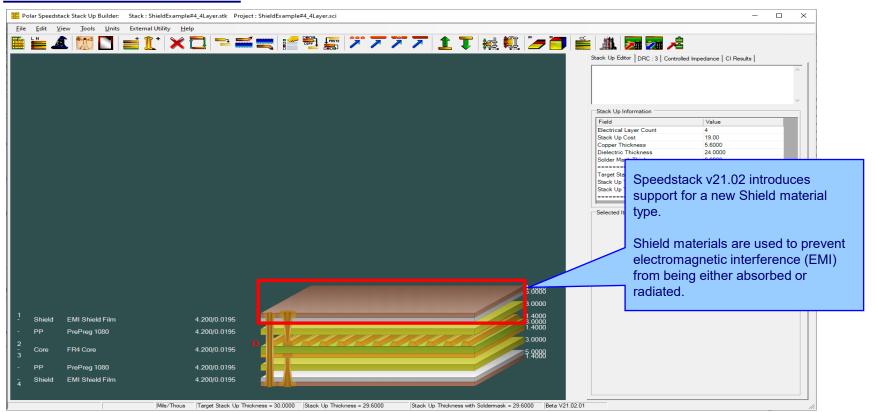
polarinstruments.com



Speedstack v21.02.01 (February 2021)



New Shield material

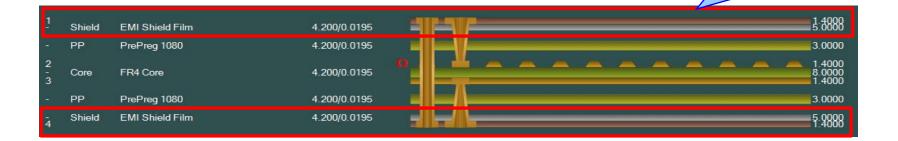






New Shield material

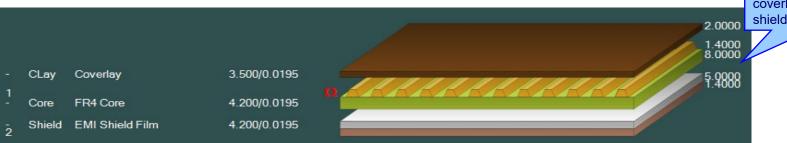
Shields are typically applied to the outer layer(s) of the stack up



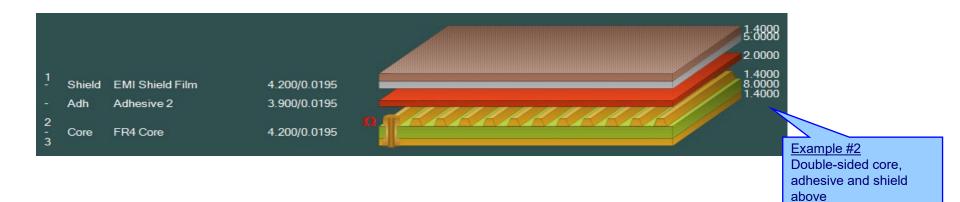
They consist of a shield layer (brown) and dielectric adhesive (silver)



Shield material examples

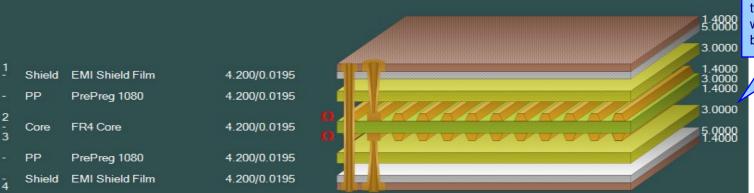


Example #1
Single-sided core,
coverlay above trace,
shield below





Shield material examples



Example #3

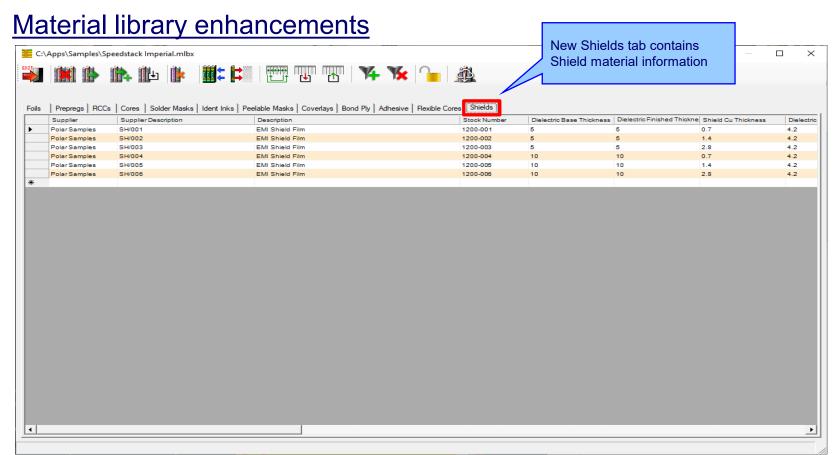
Double-sided core with two signal trace layers with shield above and below trace layers



Example #4

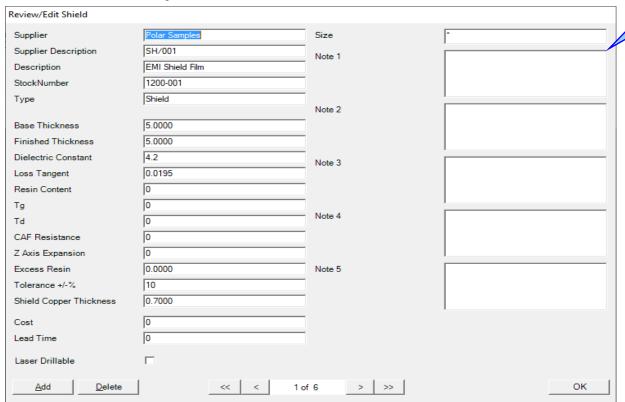
Double-sided core with one signal trace layer with shield above and below trace layers







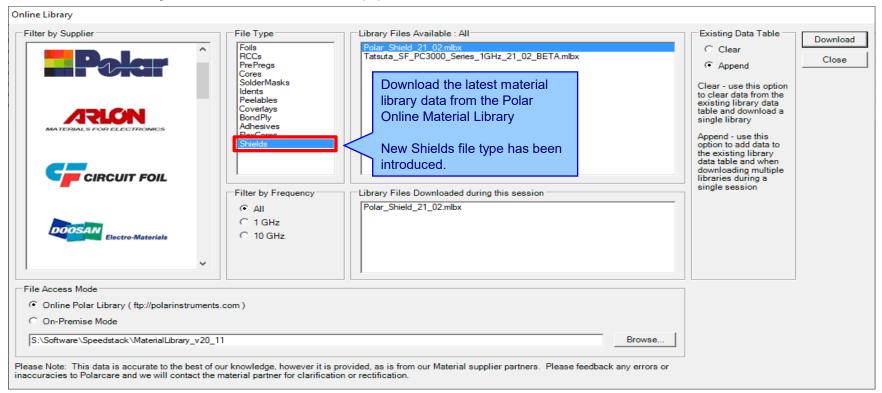
Material library enhancements



Material library Edit Shield dialog

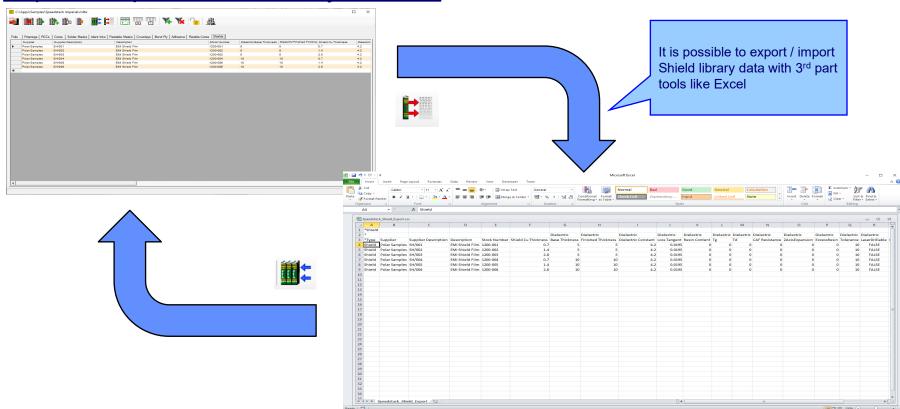


Online Library enhanced to support Shield materials



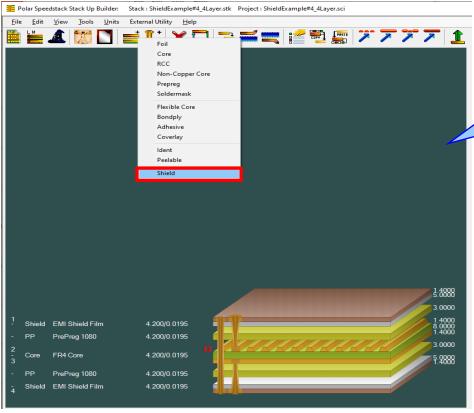


Export / Import Shield library to Excel





Stack up editor enhancements

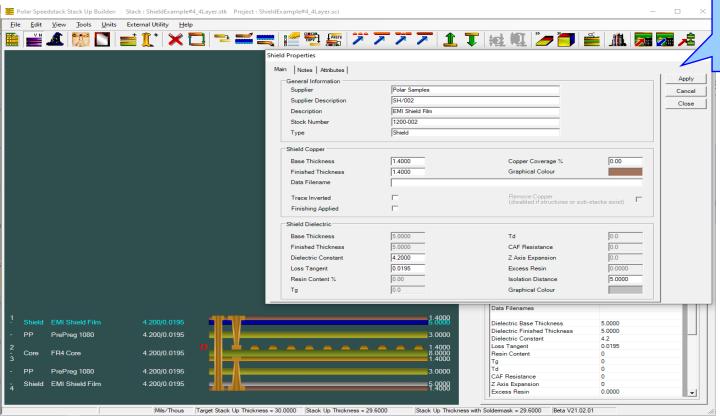


Stack Up editor enhancements:

Shield material options to add, delete, swap, move up, move down, symmetry and set properties



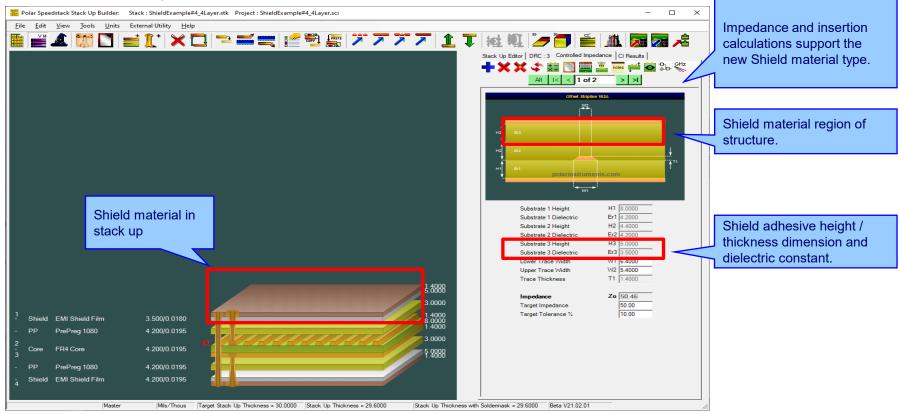
Shield properties



View and customise the Shield properties. Useful in 'what-if' scenarios

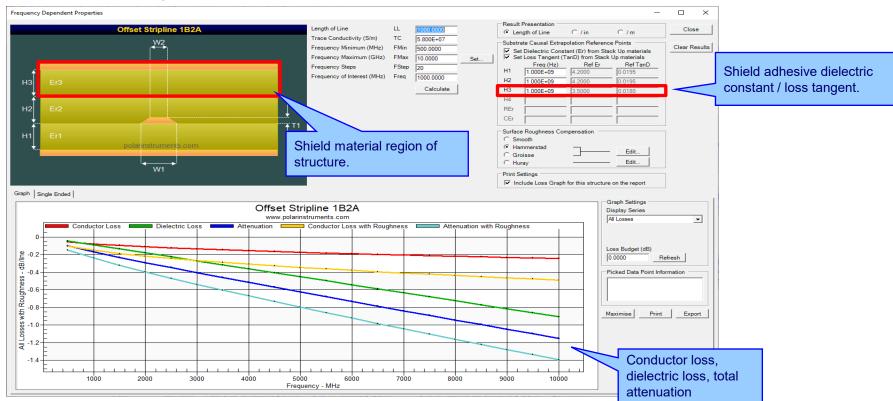


Controlled impedance and insertion loss calculations





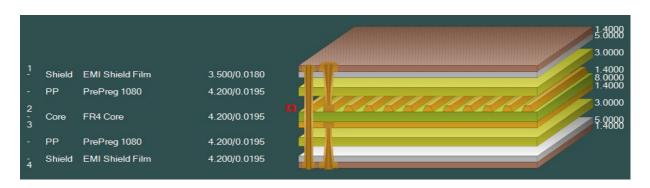
Controlled impedance and insertion loss calculations





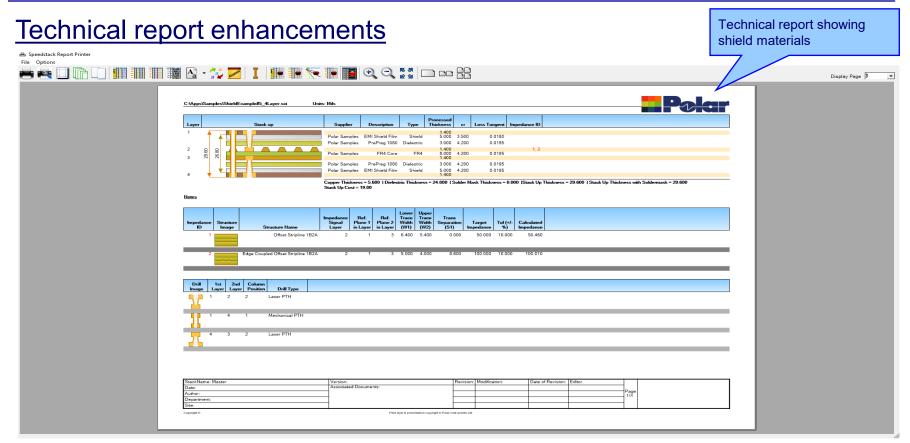
Controlled impedance and insertion loss calculations

Please note: Speedstack is capable of supporting many shield types for stack up design and documentation. However, it is important to use the correct type of shield material for controlled impedance and insertion loss applications. They are often designated by the shield vendor as 'for high speed signal transmission applications'.



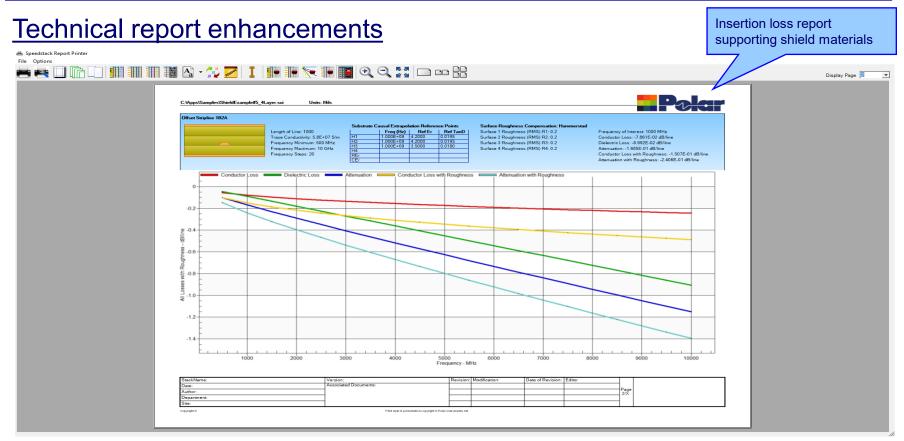












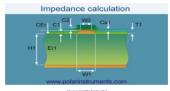


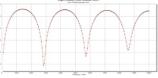
Import / Export enhancements

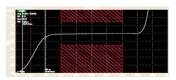
The following Import / Export options have been updated to support the new shield material introduced with Speedstack 2021:

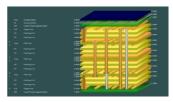
- XML STKX v20.00 and SSX v10.00 import / export options
- CSV export option
- Gerber / DXF export option



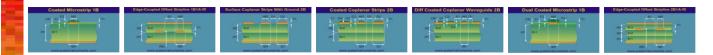












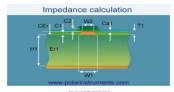
Thank you for viewing this Speedstack 2021 preview. If you have questions we would be delighted to help you. Your local contact information is contained on the

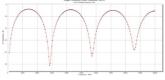


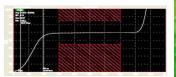
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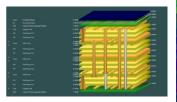
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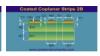






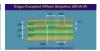












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