











# Coated Microstrip 18 Edge-Coupled Othet Singline 18/Ark Surface Coplanar Strips 28 Oth Coated Coplanar Waveguide 28 Dual Coated Microstrip 18 Edge-Coupled Othet Singline 29 Edge-Coupled

# Speedstack 2021 Preview

Richard Attrill – May 2021 (Rev 3)



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# Introducing Speedstack 2021

Welcome to a preview of Speedstack 2021.

We have introduced a number of new features that have been requested through our Polarcare software maintenance service.

If you would like to have a web-based demonstration please contact your local Polar office, details are shown on the last slide of this presentation.

Please note: the Speedstack units have been set to Mils in the following screen grabs



# Speedstack v21.05.06 (May 2021)







Foil Pr	roperties							
Mair	Notes Attributes							
Г	General Information					Apply		
	Supplier	Polar Samples						
	Supplier Description	Supplier Description FO/001				Foil Properties		
	Description		Cost	1.00	The new Laver Name property exists			
	Stock Number	100-001				on all materials with an electrical /		
	Туре	Copper		Lead Time	0.00	copper layer. The user can key in any		
	Copper					alphanumeric name		
	Base Thickness	0.7000	Copper Coverage %	6	0.00			
	Finished Thickness	1.4000	Graphical Colour					
	Layer Name	Тор						
	Data Filename							
	Trace Inverted		Remove Copper					
	Finishing Applied		(disabled if structur	es of sub-stact	rs exist)			

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Core Prope	erties							
Main	Notes Attributes							
Gene	eral Information						Apply	
Sup	pplier	Polar Samples		Exchange	e Copper 🛛 🗖	0	Close	
Sup	pplier Description	CO/005						
De	scription	FR4 Core		Cost	5.00			
Sto	ock Number	400-005		Tolerance	0.00		<u> </u>	Core Properties
Тур	ре	FR4		Lead Time	0.00			
	er Copper					-	F	or core materials, a new Layer Name
Bas	se Thickness	1.4000	Copper Coverage	%	0.00		۲ (	property has been added for both
Fin	nished Thickness	1.4000	Graphical Colour				/ ι	ipper and lower electrical / copper
Lay	yer Name	Inner 2					— li	ayers
Dat	ta Filename							
Tra	ace Inverted		Remove Copper					
Fin	nishing Applied		(disabled if structi	Jres or sub-stac	ks exist)			
Diele	ectric							
Bas	se Thickness	3.0000	Td		0.0			
Fin	iished Thickness	3.0000	CAF Resistance		0.0			
Die	electric Constant	4.2000	Z Axis Expansion		0.0			
Los	ss Tangent	0.0195	Excess Resin		0.0000			
Rea	sin Content %	60.00	Isolation Distance		3.0000			
Tg		180.0	Graphical Colour					
Lowe	er Copper							
Bas	se Thickness	1.4000	Copper Coverage	%	0.00			
Fin	iished Thickness	1.4000	Graphical Colour					
Lay	yer Name	Inner 3						
Dat	ta Filename							
Tra	ace Inverted	$\overline{\vee}$	Remove Copper					
Fin	nishing Applied		(alaabica ii alfuoli		na what			

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## New Layer Name property for electrical / copper layers



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# New Layer Name property for electrical / copper layers





# New Layer Name property for electrical / copper layers





 $\times$ 

User selectable plating

Simple % methods)

Edit Set

Cancel

thicknesses under Finishing Options (Copper Coverage &

# **Copper Finishing classes increased**

Copper Coverage Based Prepreg Corrections	×	Copper Coverage Based Prep	preg Corrections	
Percentage Copper To Be Embedded in Prepreg		Percentage Copper To Be Embe	added in Prepreg	
Set by Layer type		Set by Layer type		
Signal Layer % 75		Signal Layer	% 75	ord
Mixed Layer % 15		Mixed Layer	<sup>%</sup> 15 thic	ckn
Plane Layer % 5		Plane Layer	% 5 Op	tio
C Proportional to Coverage		C Proportional to Coverage	Sin	прі 7
Copper Finishing		Copper Finishing		
Enter values of thickness according to preference. The selected value will be the one added to the base thickness of copper layers when plating.		Enter values of thickness accor the one added to the base thickn	iding to preference. The selected value w ness of copper layers when plating.	vill be
Class Name Value Selection		ID   Class Name	Class Value   Active	
Class 1 0.7000 •		1 Class 1	0.7000 YES	
Class 2 0 7000 C		2 Rich	0.8000	
[chao 2 ] [c.7000 ] [C.700		3 Class 3	0.7000	
		4 Class 4	0.7000	
Class 4  0.7000		5 Class 5	0.0000	
Excess Resin Test		Excess Resin Test		
Minimum Excess Resin % 15		Minimum Excess Resin	% 15	
Apply Cancel			Apply	

Speedstack v21.04 and earlier supported 4 classes

Speedstack v21.05 now supports 20 classes



# Import / Export enhancements

The following Import / Export options have been updated to support the layer name property introduced with Speedstack v21.05.06:

- XML STKX v21.00 and SSX v11.00 import / export options
- CSV export option
- Gerber / DXF export option



# Speedstack v21.04.00 (April 2021)



# All Losses plot - clearer labelling



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# Other enhancements

- The controlled impedance and insertion loss Calculation Engine
  updated to the latest edition
- Frequency Dependent Calculations graphing library enhancements



# Speedstack v21.03.09 (March 2021)





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## New Apply Plating Colours toolbar option



#### Plated Copper Layers

During PCB fabrication drill holes commonly have copper applied to the barrel wall by an electroplating process. This provides an interconnect between copper layers in the stack up.

This electroplating process often results in additional copper also being applied to the exposed copper layers where the mechanical drill starts / ends.

It is important to account for this additional plated copper thickness when calculating the overall stack up thickness and controlled impedance / insertion loss structures.

Speedstack has always allowed this additional plating thickness to be applied to the relevant copper layers. With v21.03 this has been enhanced further with automatic colour assignments to the plated and unplated layers

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## New Apply Plating Colours toolbar option



### **Standard Colours**



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### **Apply Plating Colours**





# New Apply Plating Colours toolbar option





# New Apply Plating Colours toolbar option







### Online Library enhancements

Online Library





# Speedstack v21.02.01 (February 2021)



# New Shield material











### Speedstack 2021 Introduction



![](_page_26_Picture_0.jpeg)

![](_page_26_Figure_2.jpeg)

![](_page_27_Picture_0.jpeg)

#### Material library enhancements New Shields tab contains C:\Apps\Samples\Speedstack Imperial.mlbx $\times$ Shield material information 🏙 🆫 🎠 🏨 🕌 🇮 📜 🔚 🛅 🏙 🆓 EXIT Prepregs RCCs Cores Solder Masks Ident Inks Peelable Masks Coverlays Bond Ply Adhesive Flexible Cores Shields Foils Supplier Supplier Description Description Stock Number Dielectric Base Thickness | Dielectric Finished Thickne | Shield Cu Thickness Dielectric SH/001 EMI Shield Film 1200-001 5 5 0.7 4.2 | ⊾ Polar Samples SH/002 EMI Shield Film 1200-002 5 5 1.4 4.2 PolarSamples SH/003 EMI Shield Film 1200-003 5 2.8 4.2 Polar Samples 5 SH/004 EMI Shield Film 10 10 0.7 4.2 Polar Samples 1200-004 Polar Samples SH/005 EMI Shield Film 1200-005 10 10 1.4 4.2 2.8 Polar Samples SH/006 EMI Shield Film 1200-006 10 10 4.2 | -) • ъI

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![](_page_28_Picture_0.jpeg)

Review/Edit Shield				dialog
Supplier	Polar Samples	Size	*	
Supplier Description	SH/001	Note 1		
Description	EMI Shield Film			
StockNumber	1200-001			
Туре	Shield			
		Note 2		
Base Thickness	5.0000			
Finished Thickness	5.0000			
Dielectric Constant	4.2	Note 3		
Loss Tangent	0.0195			
Resin Content	0			
Tg	0			
Td	0	Note 4		
CAF Resistance	0			
Z Axis Expansion	0			
Excess Resin	0.0000	Note 5		
Tolerance +/-%	10			
Shield Copper Thickness	0.7000			
Cost	0			
Lead Time	0			
Laser Drillable				

### . . . . - -

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![](_page_29_Picture_0.jpeg)

![](_page_29_Picture_1.jpeg)

# **Online Library enhanced to support Shield materials**

![](_page_29_Figure_3.jpeg)

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![](_page_30_Picture_0.jpeg)

# Export / Import Shield library to Excel

![](_page_30_Figure_3.jpeg)

![](_page_31_Picture_1.jpeg)

# Stack up editor enhancements

![](_page_31_Picture_3.jpeg)

### Stack Up editor enhancements:

Shield material options to add, delete, swap, move up, move down, symmetry and set properties

![](_page_32_Picture_0.jpeg)

# **Shield properties**

Polar Sp <u>F</u> ile <u>E</u> dir	eedstack Stack Up t <u>V</u> iew <u>T</u> ools	Builder: Stac	k∶ShieldExample# rnal Utility <u>H</u> elp	4_4Layer.stk Proj	ect : Shieldl	xample#4_4Layer.sci				_	- 🗆 X	View and customise the Shield properties Useful
	1		1 🗙	<u> </u>	1	💒 📆 📲 🌶	* 7 7 7	<b>1 T</b>	短 🏨 🥭 📑	🎽 🥼 🗩	<mark>──</mark> 📌 ∕	in 'what-if' scenarios
			_		Shie	ld Properties						1
					M	ain Notes Attributes						
						General Information					Apply	
						Supplier	Polar Samples				Cancel	
						Supplier Description	SH/002				Close	
						Description	EMI Shield Film					
						Stock Number	1200-002					
						Туре	Shield					
						Shield Copper						
						Base Thickness	1.4000		Copper Coverage %	0.00		
						Finished Thickness	1.4000		Graphical Colour			
						Data Filename			•			
						Transformed			Remove Copper			
						Finishing Applied	-		(disabled if structures or su	b-stacks exist)		
						Finishing Applied	I					
						Shield Dielectric						
						Base Thickness	5.0000		Td	0.0		
						Finished Thickness	5.0000		CAF Resistance	0.0		
						Dielectric Constant	4.2000		Z Axis Expansion	0.0		
						Loss Tangent	0.0195		Excess Resin	0.0000		
						Resin Content %	0.00		Isolation Distance	5.0000		
						Tg	0.0		Graphical Colour			
								_	Data Filenames			1
1			4 200/0 0105				1.4	1000				
- Shiel	a EMI Shield Fi		4.200/0.0195				5.0	000	Dielectric Base Thickness Dielectric Einished Thickness	5.0000		
- PP	PrePreg 1080		4.200/0.0195				3.0	0000	Dielectric Constant	4.2		
2 - Coro	FR4 Core		4 200/0 0195					1000	Loss Tangent	0.0195		
3	I NA COIE		4.200/0.0195				0.0 1.4	4000	Tg	0		
- PP	PrePreg 1080		4.200/0.0195				3.(	0000	Td	0		
- Shiel	d EMI Shield Ei	lm	4 200/0 0195				5 (	0000	CAF Resistance Z Axis Expansion	0		
4							1.2	1000	Excess Resin	0.0000	•	
J			Mils/Thous	Target Stack Up T	hickness = 3	0.0000 Stack Up Thickness	= 29.6000 Stack U	lp Thickness with S	Soldermask = 29.6000 Beta V21.0	2.01	1	2

![](_page_33_Picture_0.jpeg)

# **Controlled impedance and insertion loss calculations**

![](_page_33_Figure_3.jpeg)

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![](_page_34_Picture_0.jpeg)

# **Controlled impedance and insertion loss calculations**

![](_page_34_Figure_3.jpeg)

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![](_page_35_Picture_0.jpeg)

# **Controlled impedance and insertion loss calculations**

Please note: Speedstack is capable of supporting many shield types for stack up design and documentation. However, it is important to use the correct type of shield material for controlled impedance and insertion loss applications. They are often designated by the shield vendor as 'for high speed signal transmission applications'.

![](_page_35_Figure_4.jpeg)

![](_page_36_Picture_0.jpeg)

![](_page_36_Figure_2.jpeg)

![](_page_37_Picture_0.jpeg)

### Speedstack 2021 Introduction

![](_page_37_Figure_2.jpeg)

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![](_page_38_Picture_1.jpeg)

# Import / Export enhancements

The following Import / Export options have been updated to support the new shield material introduced with Speedstack 2021:

- XML STKX v20.00 and SSX v10.00 import / export options
- CSV export option
- Gerber / DXF export option

![](_page_39_Picture_0.jpeg)

![](_page_39_Figure_1.jpeg)

 Coated Microstrip 1B
 Edge-Coupled Offset Stripter 1B1ARR
 Surface Coplanar Strips Vith Ground 2B
 Coated Coplanar Strips 2B
 Diff Coated Coplanar Waveguide 2B
 Dual Coated Microstrip 1B
 Edge-Coupled Offset Stripter 2BIARR

 Image: Comparing the stripter 1B
 Image: Comparing the stripter 2B
 Ima

Thank you for viewing this Speedstack 2021 preview. If you have questions we would be delighted to help you. Your local contact information is contained on the following slide

![](_page_39_Figure_4.jpeg)

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![](_page_40_Picture_0.jpeg)

![](_page_40_Figure_1.jpeg)

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